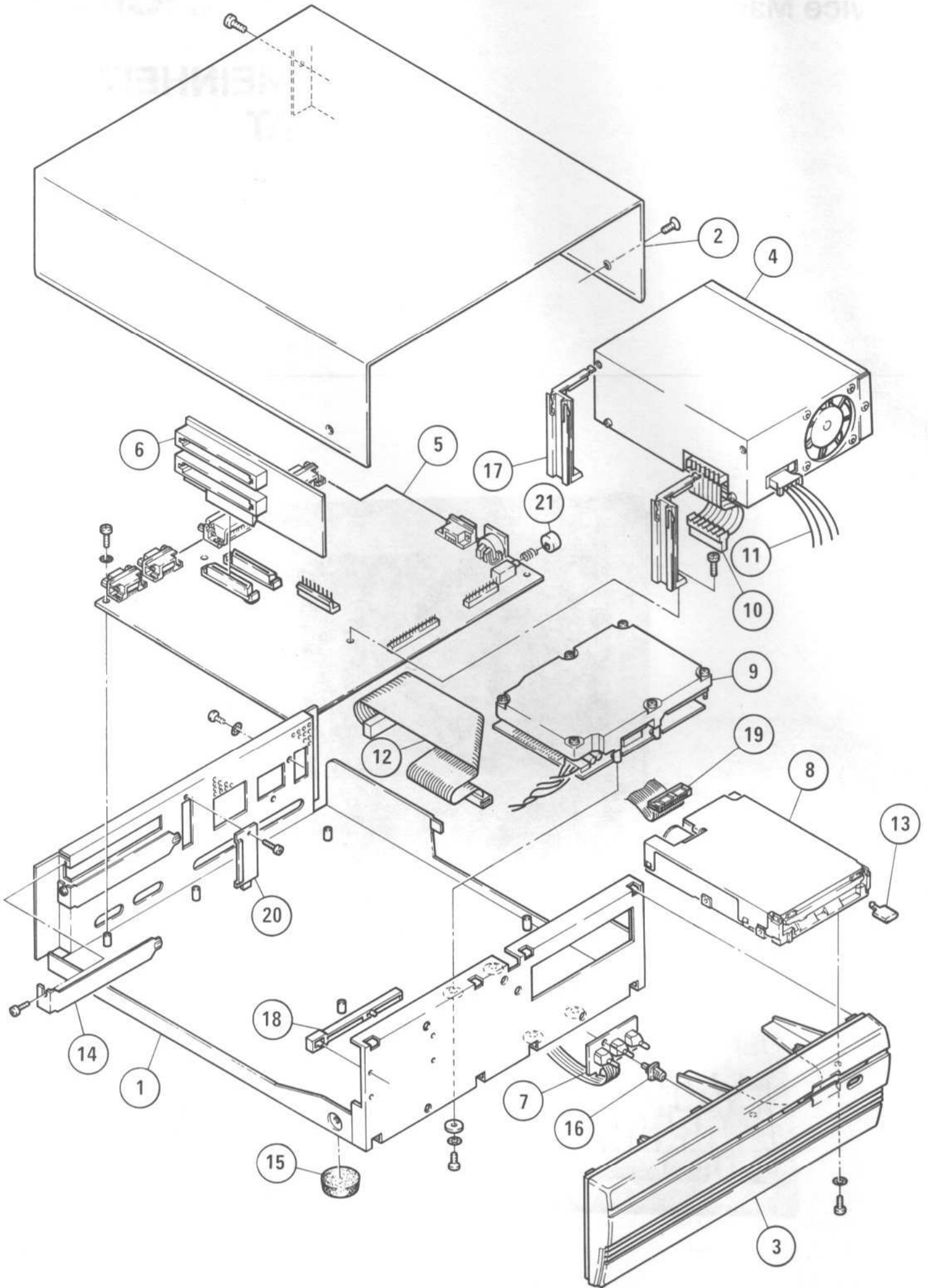


**SYSTEMEINHEIT**  
**EURO AT**

Ident-Nr.: 43490



# Exploded view

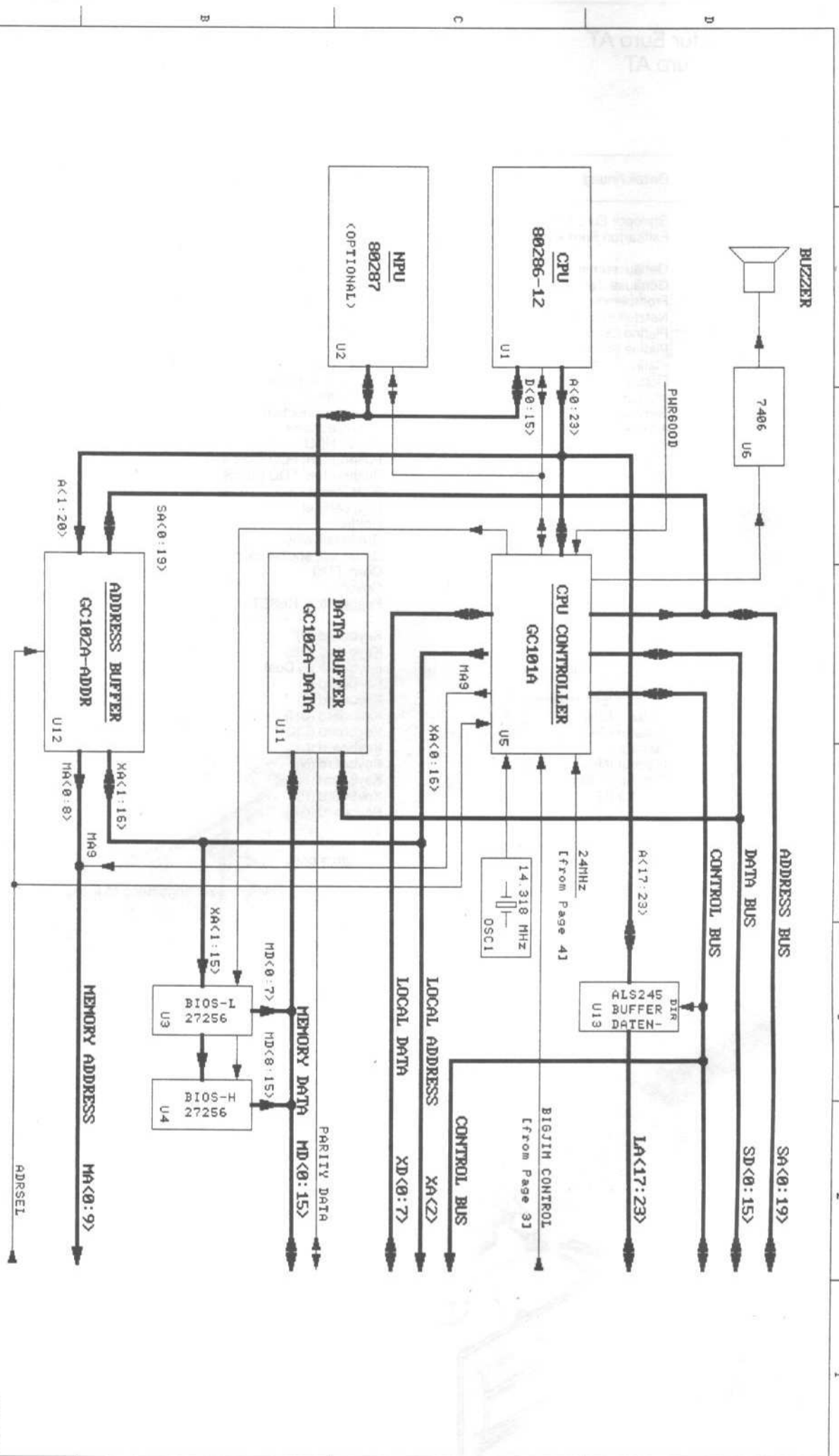


Ersatzteilliste für Euro AT  
Parts List for Euro AT

Best.-Nr. Part.-No.	Bezeichnung	Description	Zeich.-Pos. Ref.-No.	Preisgruppe
51 451 00	Styropor Euro XT/AT	Polyfoam		C 1
51 452 00	Faltkarton Euro XT/AT	Gift box		C 0
51 254 00	Gehäuseunterteil	Cabinet, bottom	1	D 4
51 257 00	Gehäuseoberteil	Cabinet, top	2	D 2
51 462 00	Frontblende	Front, mask	3	C 3
51 467 00	Netzteil Euro XT	Power supply	4	F 9
51 513 00	Platine CPU	P.C.B. CPU	5	*
51 620 00	Platine Steckplatz	P.C.B. slot	6	D 8
52 122 00	Platine LED	P.C.B. LED	7	C 2
50 021 00	Floppylaufwerk	Floppy disk drive	8	G 7
51 550 00	HD-Laufwerk	Hard disk drive	9	J 2
51 430 00	Verbindungskabel, Netz-CPU	Cabel, connection	10	B 3
51 458 00	Adapterkabel HD 4pol.	Cabel, adapter	11	B 4
51 444 00	HD-Kabel	Cabel, HDD	12	C 0
50 056 00	Druckknopf für FDD (Toshiba)	Pushbutton, FDD (Toshiba)	13	A 5
50 294 00	Druckknopf für FDD (Sony)	Pushbutton, FDD (Sony)	13	A 5
50 020 00	Blindblech	Plate, blind	14	A 7
51 456 00	Gehäusefuß	Leg, cabinet	15	A 4
51 457 00	Platinenhalter	Holder	16	A 3
51 262 00	Führungsschiene Steckplatz	Guide rail, slot	17	A 6
50 865 00	Führungsschiene Steckkarte	Guide rail, socket card	18	A 6
51 443 00	Kabelbaum FDD	Cord, FDD	19	B 6
51 266 00	Abdeckung	Cover	20	A 5
51 630 00	Druckknopf RESET	Pushbutton, RESET	21	A 6
51 750 00	Tastatur (D)	Keyboard (D)		F 6
51 751 00	Tastatur (GB)	Keyboard (GB)		F 6
51 752 00	Tastatur (F) Dual	Keyboard (F) Dual		F 6
51 753 00	Tastatur (F)	Keyboard (F)		F 6
51 754 00	Tastatur (E)	Keyboard (E)		F 6
51 755 00	Tastatur (CH)	Keyboard (CH)		F 6
51 756 00	Tastatur (DK)	Keyboard (DK)		F 6
51 757 00	Tastatur (I)	Keyboard (I)		F 6
51 758 00	Tastatur (N)	Keyboard (N)		F 6
51 759 00	Tastatur (S/SF)	Keyboard (S/SF)		F 6
51 760 00	Tastatur (P)	Keyboard (P)		F 6
51 761 00	Tastatur (GR)	Keyboard (GR)		F 6

**Note:** All spare parts with countryindex are reserved for foreign distributors only.

All 3.5" and 5.25" floppydisks must be replaced by new ones. Schneider passes no exchange price to the distributor or dealer.

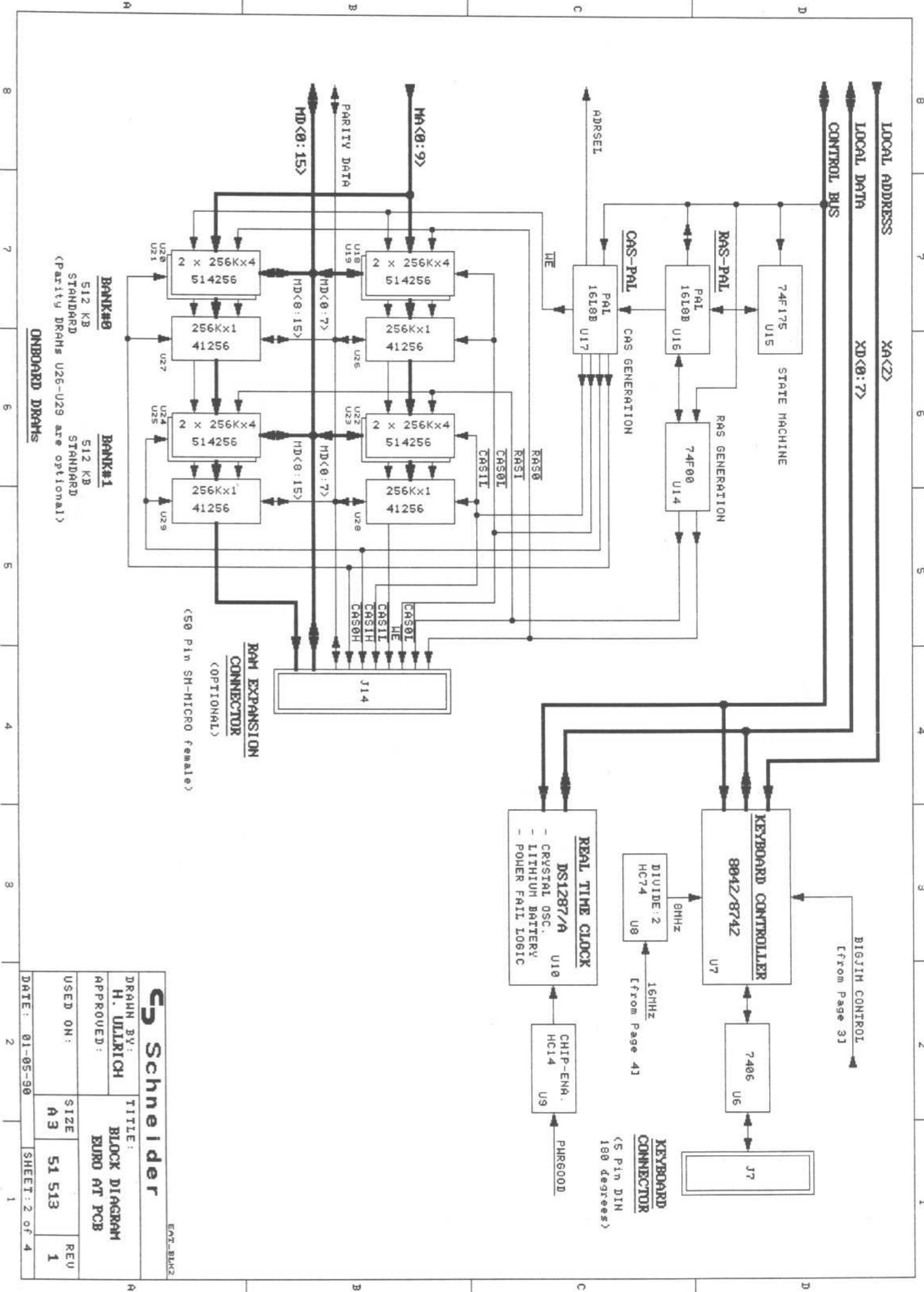


**Schneider**  
 ENT-BK1

DRÄHN BV:  
 H. ULLRICH  
 APPROVED:  
 TITLE:  
 BLOCK DIAGRAM  
 EURO AT PCB

USED ON:	SIZE	REV
A3	51 513	1

DATE: 01-05-90  
 SHEET: 1 of 4



BIGJIM CONTROL  
[from Page 31]

KEYBOARD CONNECTOR  
(5 Pin DIN 180 degrees)

**Schneider**

DRAWN BY: H. ULLRICH  
TITEL: BLOCK DIAGRAM  
APPROVED: BURD AT PCB

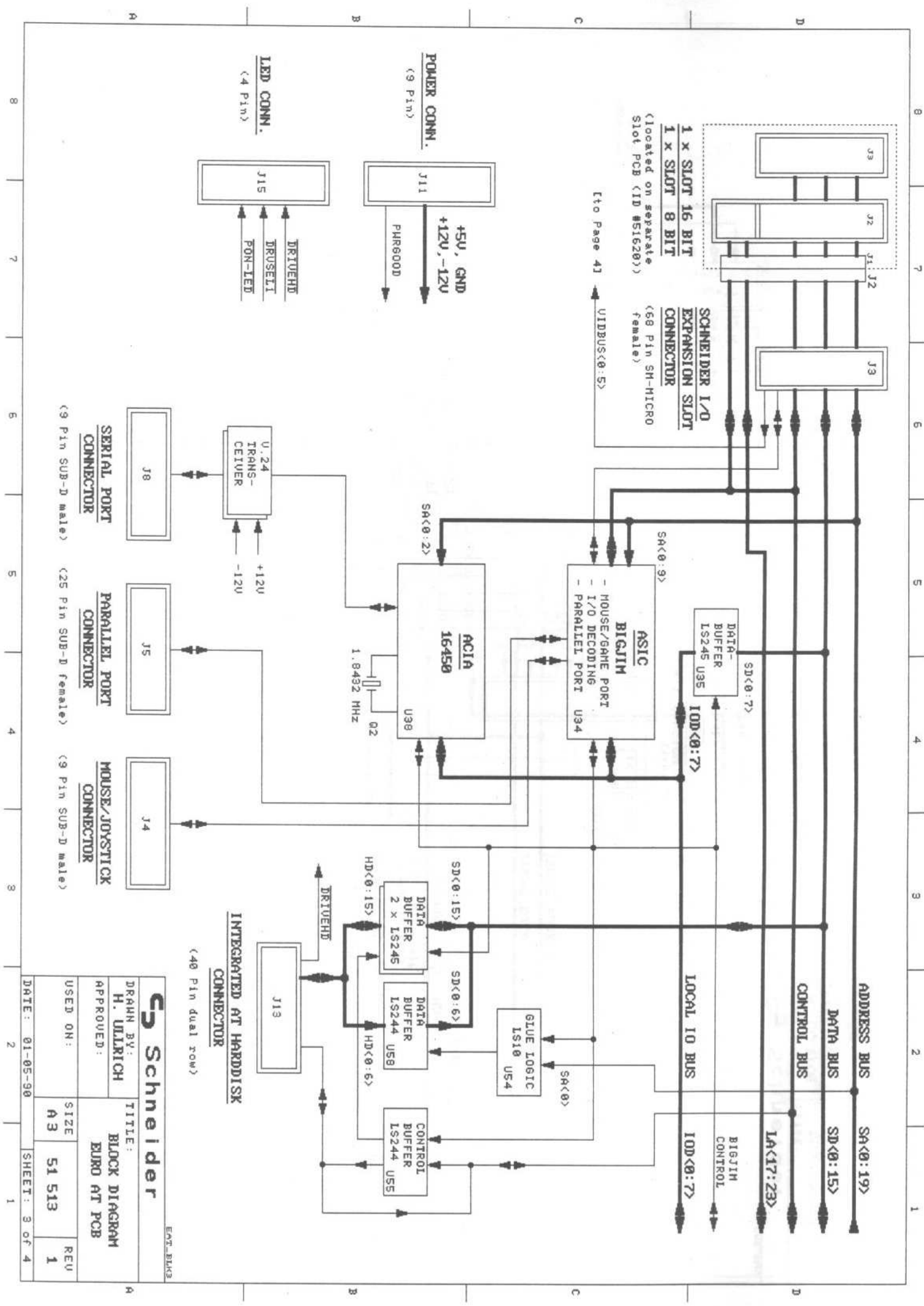
USED ON: SIZE A3  
DATE: 01-05-90 SHEET: 2 of 4

REV 1

A B C D

8 7 6 5 4 3 2 1

A B C D

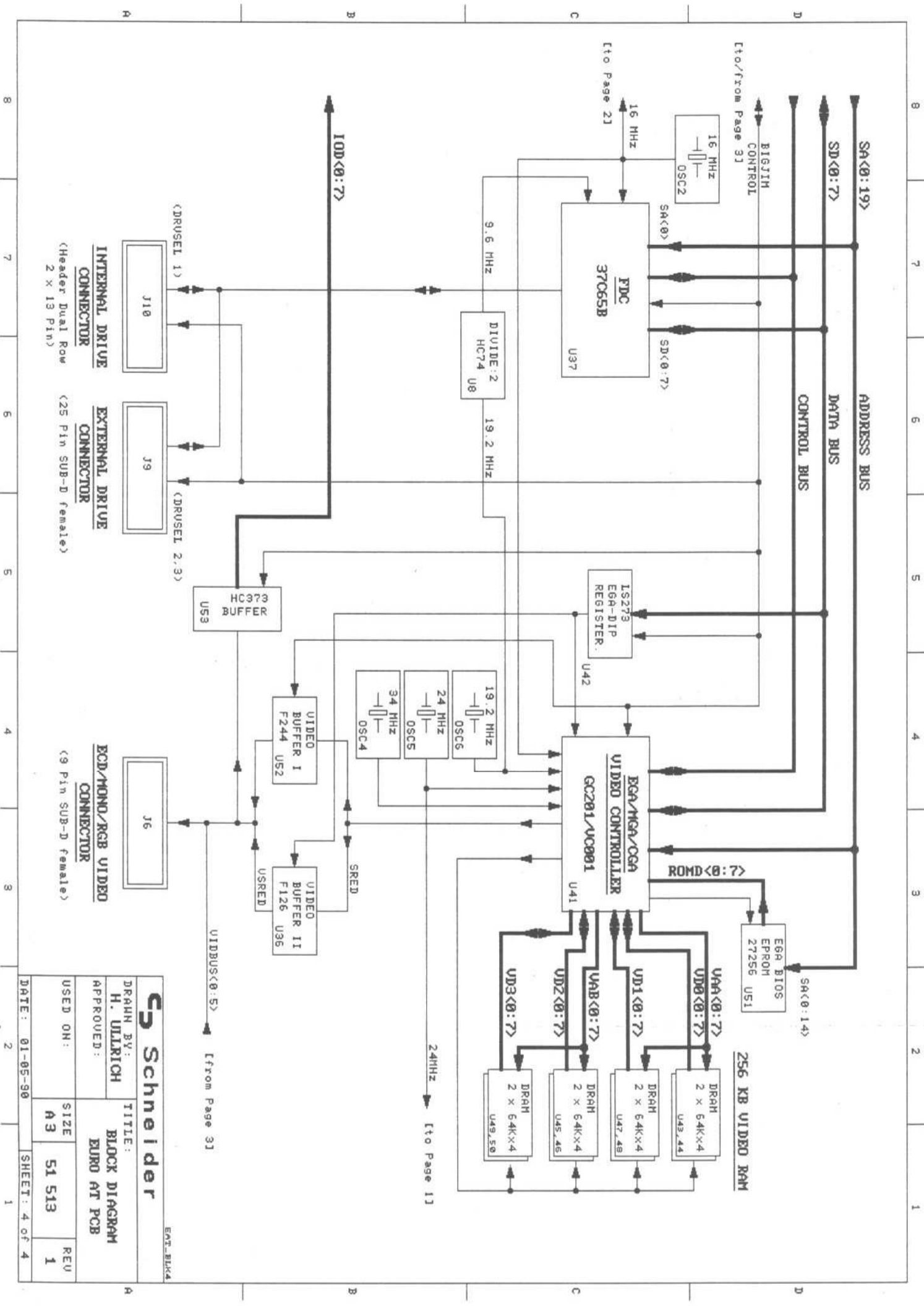


**Schneider**  
 ENT-BK3

**DRAWN BY:** H. ULLRICH  
**APPROVED:**  
**TITLE:** BLOCK DIAGRAM EURO AT PCB

**USED ON:** SIZE A3  
 51 513  
 REV 1

**DATE:** 01-05-90  
**SHEET:** 3 of 4



INTERNAL DRIVE CONNECTOR (Header Dual Row 2 X 13 Pin)

EXTERNAL DRIVE CONNECTOR (25 Pin SUB-D female)

ECD/MONO/RGB VIDEO CONNECTOR (9 Pin SUB-D female)

**Schneider**  
 ENT-BLK4

DRAWN BY: H. ULRICH  
 APPROVED: [Signature]  
 TITLE: BLOCK DIAGRAM EURO AT PCB

USED ON: SIZE A3  
 DATE: 01-05-90 SHEET: 4 of 4

REV 1

24MHz [to Page 1]

VIDBUS<0:5> [from Page 3]

8 7 6 5 4 3 2 1

8 7 6 5 4 3 2 1

REVISIONS

LTR	DESCRIPTION	DATE	APPROVED
5	PCB REVISION 1	12-07-89	K. WINZEK
6	PCB REVISION 1A, OSC1 REPLACES Q1	12-07-89	K. WINZEK
7	PCB REV. 1B, C202 NOT USED	01-12-90	K. WINZEK

DRAWING FILES:

EAT\_71.DWG  
 EAT\_72.DWG  
 EAT\_73.DWG  
 EAT\_74.DWG  
 EAT\_75.DWG  
 EAT\_76.DWG  
 EAT\_77.DWG  
 EAT\_78.DWG  
 EAT\_79.DWG  
 EAT\_710.DWG  
 EAT\_711.DWG  
 EAT\_712.DWG  
 EAT\_713.DWG  
 EAT\_714.DWG  
 EAT\_715.DWG  
 EAT\_716.DWG  
 EAT\_717.DWG  
 EAT\_718.DWG

CONTENTS:

THIS SHEET  
 CPU SECTION  
 BIOS EPROMS  
 GC 101 A  
 KEYBOARD / CLOCK  
 REAL TIME CLOCK / SPKR  
 ADDRESS / DATA BUFFERS  
 TIMING FOR MEMORY  
 MEMORY I  
 MEMORY II  
 MEMORY II  
 BIGJIM  
 FLOPPY INTERFACE  
 SERIAL INTERFACE  
 VIDEO CONTROLLER  
 VIDEO MEMORY  
 VIDEO INTERFACE  
 AT HARD DISK INTERFACE  
 SLOT PCB CONNECTOR

DRAWING FILES:

EAT\_719.DWG  
 EAT\_720.DWG  
 EAT\_721.DWG  
 EAT\_722.DWG  
 EAT\_723.DWG  
 EAT\_724.DWG

CONTENTS:

SCHNEIDER EXTENSION CONNECTOR  
 ANALOG JOYSTICK / CONNECTORS I  
 CONNECTORS II  
 CONNECTORS III  
 CONNECTORS IV  
 BLOCK CAPS.



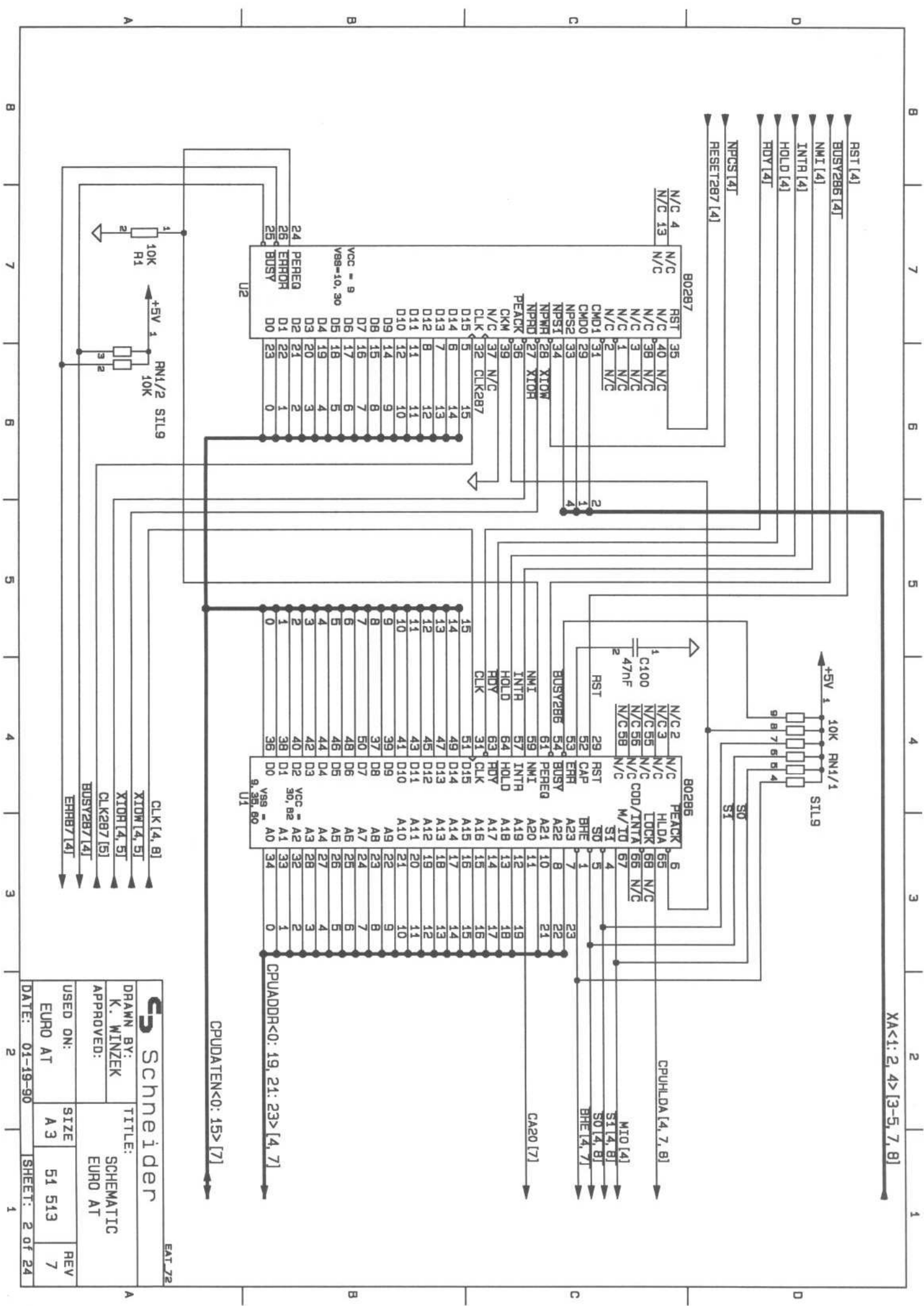
EAT\_71

DRAWN BY: K. WINZEK  
 APPROVED: EURO AT  
 TITLE: SCHEMATIC  
 EURO AT

USED ON: EURO AT  
 SIZE: A3  
 REV: 7

DATE: 01-19-90  
 SHEET: 1 of 24





XA<1: 2, 4> [3-5, 7, 8]

+5V 4 10K RN1/1  
STL9

RST [4]  
BUSY286 [4]  
NMI [4]  
INTR [4]  
HOLD [4]  
RDY [4]  
NPCS [4]  
RESET87 [4]

80287

RST 35  
N/C 4  
N/C 13

N/C 40  
N/C 38  
N/C 3  
N/C 1  
N/C 2  
CMD1 31  
CMD0 29  
NPS2 33  
NPS1 34  
NPNR 28  
NPPR 27  
PEACK 36  
CKM 39  
N/C 37  
CLK 32

XIOV 15  
XIOH 14  
13  
12  
11  
10  
9  
8  
7  
6  
5  
4  
3  
2  
1  
0

VCC = 9  
V88=10, 30

PEREG 24  
ERROR 26  
BUSY 25

U2

10K R1  
+5V 4  
RN1/2 STL9

1  
2

15  
14  
13  
12  
11  
10  
9  
8  
7  
6  
5  
4  
3  
2  
1  
0

N/C 2  
N/C 3  
N/C 55  
N/C 56  
N/C 58  
N/C

PEACK 6  
HLDA 65  
LOCK 68  
N/C 66  
N/C 67  
M/IO 67  
S1 4  
S0 5  
BHE 7  
A23 8  
A22 9  
A21 10  
A20 11  
A19 12  
A18 13  
A17 14  
A16 15  
A15 16  
A14 17  
A13 18  
A12 19  
A11 20  
A10 21  
A9 22  
A8 23  
A7 24  
A6 25  
A5 26  
A4 27  
A3 28  
A2 29  
A1 30  
A0 31  
V99 = 90

80286  
RST 29  
CAP 52  
ERR 53  
BUSY 54  
PEREG 61  
NMI 59  
INTR 57  
HOLD 64  
RDY 63  
CLK 31  
D0 36

U1

CLK [4, 8]  
XIOH [4, 5]  
XIOV [4, 5]  
CLK87 [5]  
BUSY287 [4]  
ERR87 [4]

CPUDATAEN<0: 15> [7]

CPUADDR<0: 19, 21: 23> [4, 7]

CPUHLDA [4, 7, 8]

MIO [4]  
S1 [4, 8]  
S0 [4, 8]  
BHE [4, 7]  
CA20 [7]

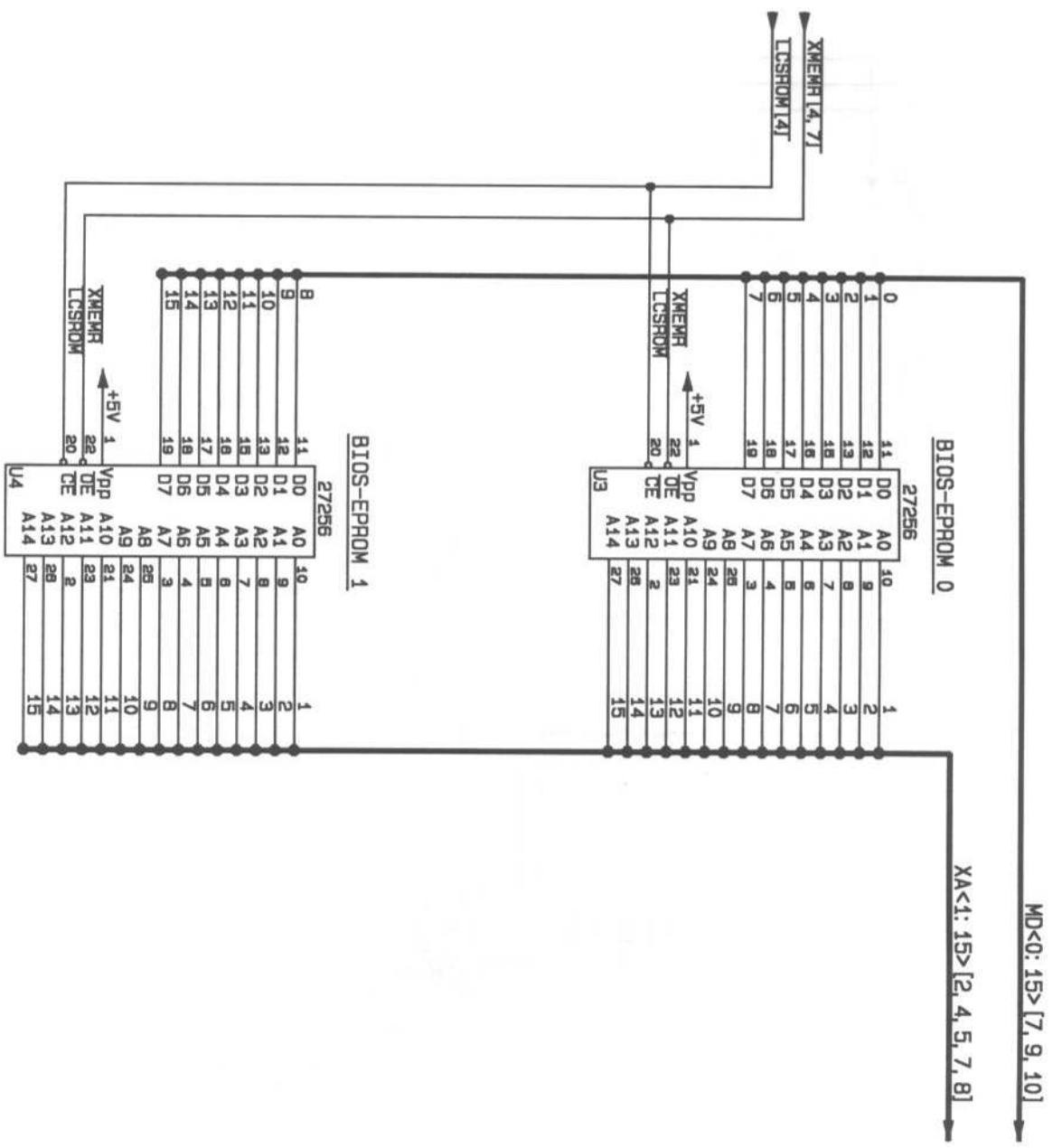
**S** Schneider

EAT 72

DRAWN BY: K. WINZEK  
APPROVED: EURO AT  
TITLE: SCHEMATIC  
EURO AT

USED ON: EURO AT  
DATE: 01-19-90  
SIZE: A3  
SHEET: 2 of 24

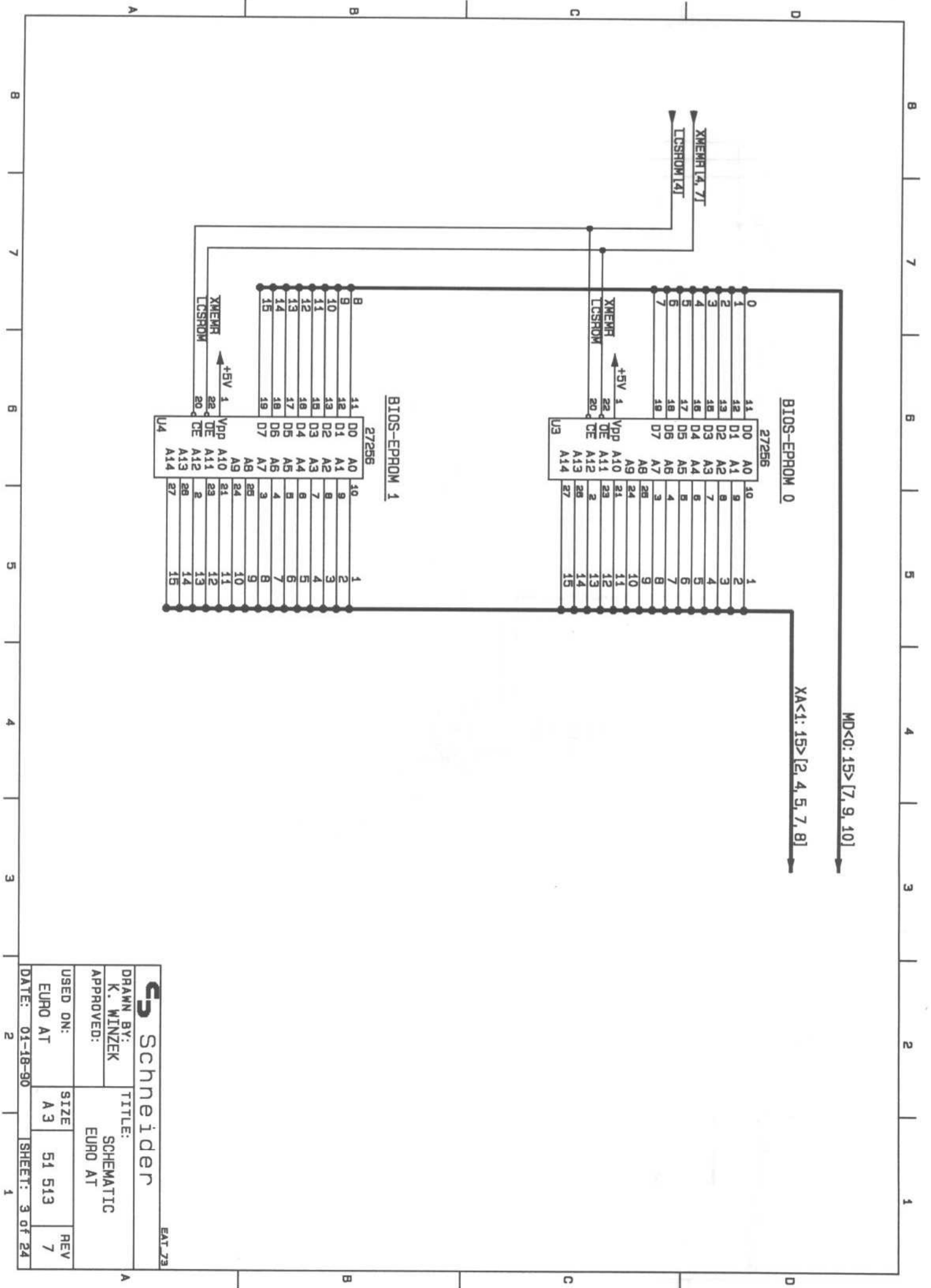
51 513  
REV 7

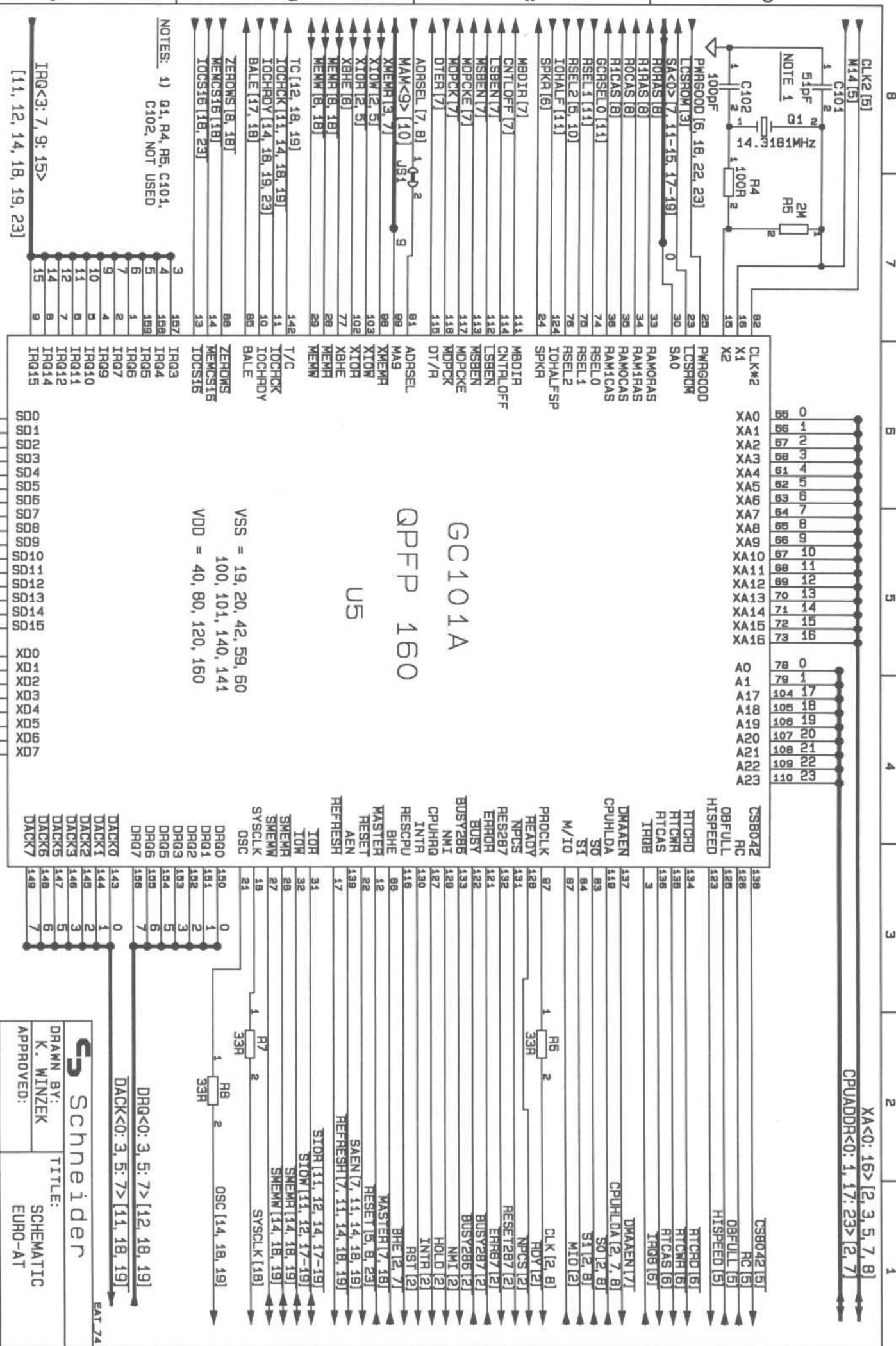


**Schneider**  
EAT 73

DRAWN BY: K. WINZEK  
TITLE: SCHEMATIC  
APPROVED: EURO AT

USED ON: EURO AT  
SIZE: A3  
DATE: 01-18-90  
SHEET: 3 of 24

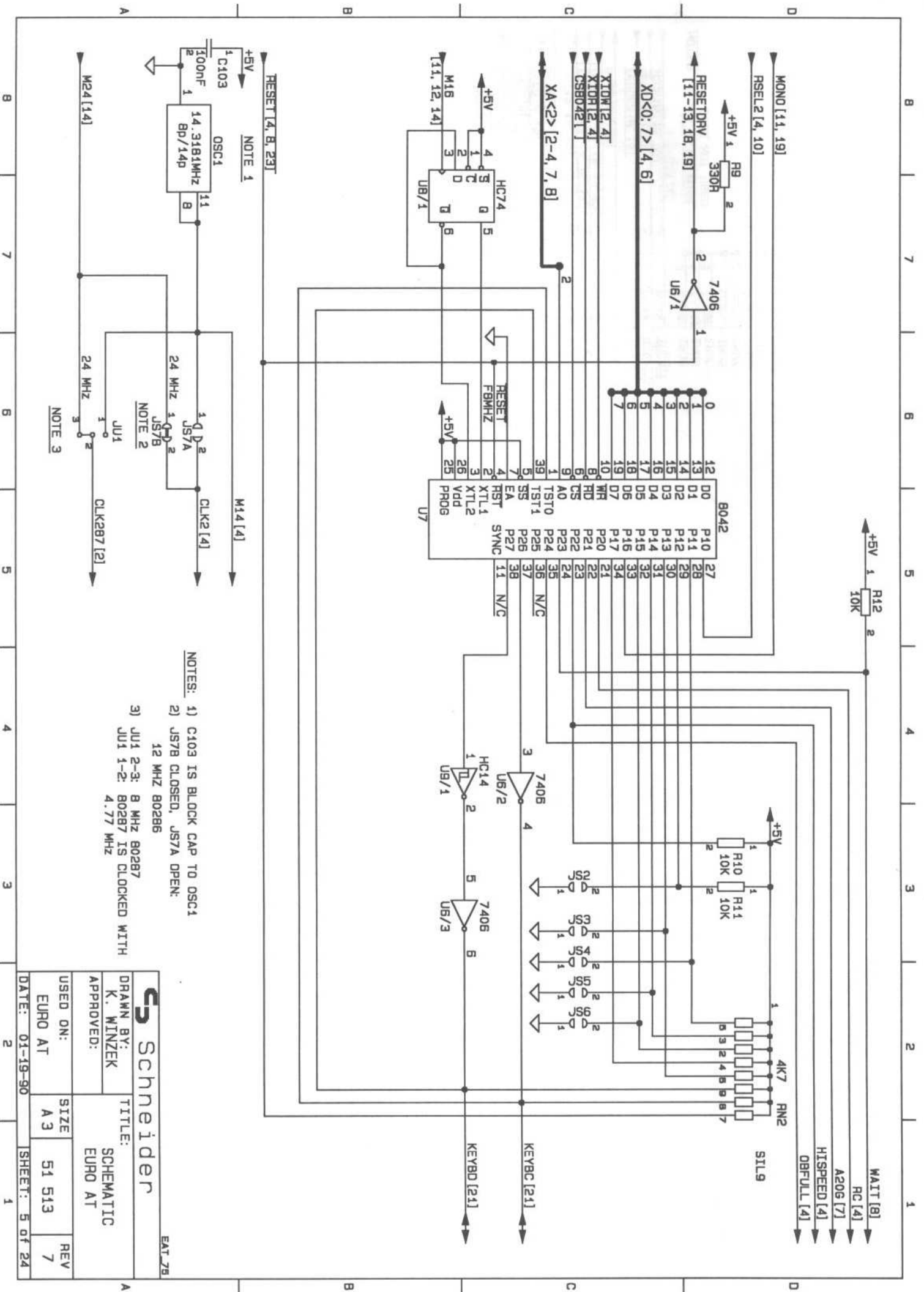




**NOTES:** 1) Q1, R4, R5, C101, C102, NOT USED  
 2) XA<0>: 16> [2, 3, 5, 7, 8]  
 CPUADDR<0>: 1, 17: 23> [2, 7]  
 VSS = 19, 20, 42, 59, 60  
 100, 101, 140, 141  
 VDD = 40, 80, 120, 160  
 DRQ<0>: 3, 5: 7> [12, 18, 19]  
 DACK<0>: 3, 5: 7> [11, 18, 19]



Schneider  
 EURO-AT

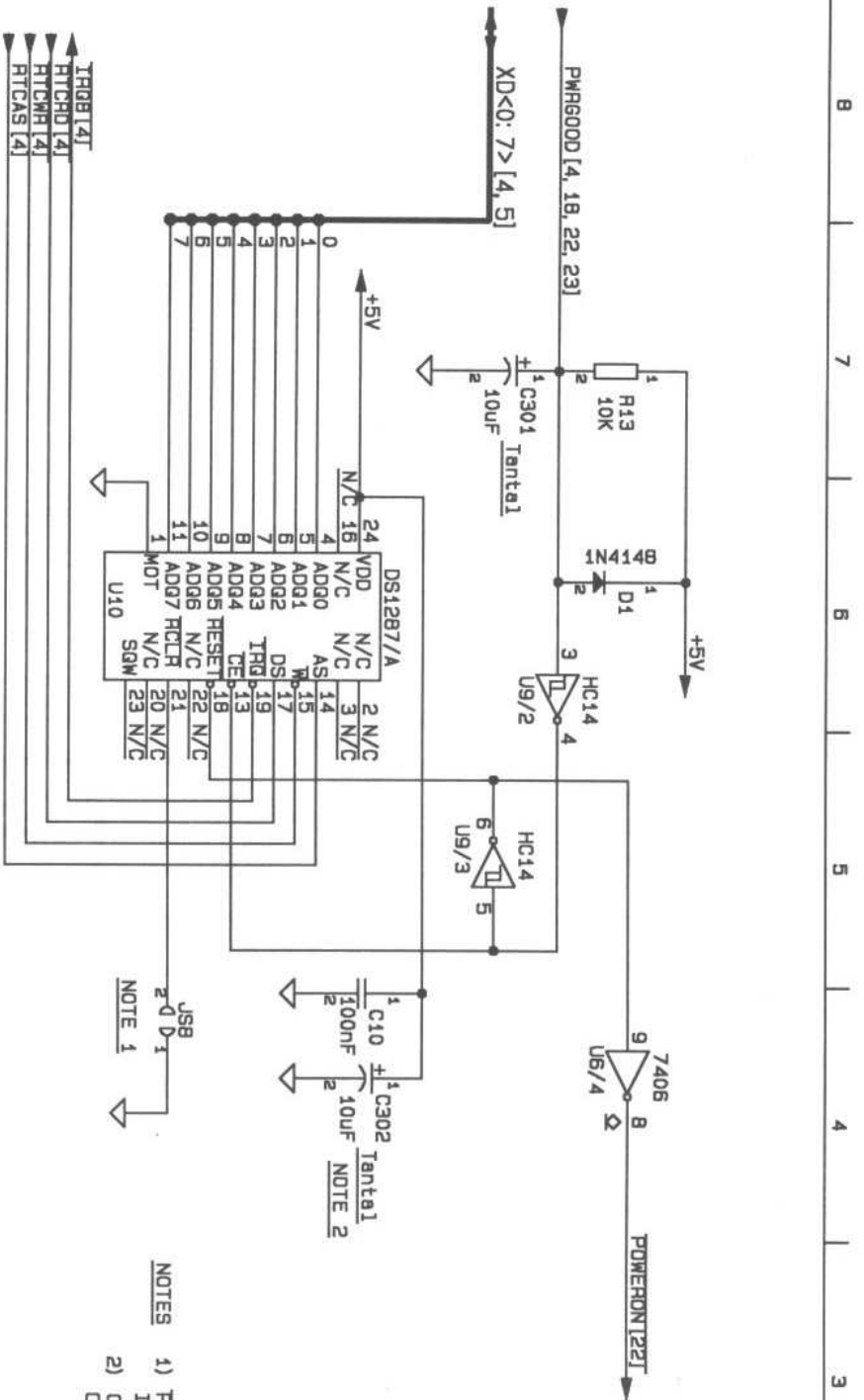


- NOTES:
- 1) C103 IS BLOCK CAP TO OSC1
  - 2) JS7B CLOSED, JS7A OPEN:  
12 MHZ 80286
  - 3) JU1 2-3: 8 MHZ 80287  
JU1 1-2: 80287 IS CLOCKED WITH  
4.77 MHZ

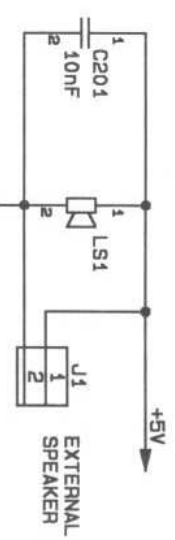
**Schneider**  
EAT 79

DRAWN BY: K. WINZEK  
APPROVED: \_\_\_\_\_  
TITLE: SCHEMATIC  
EURO AT

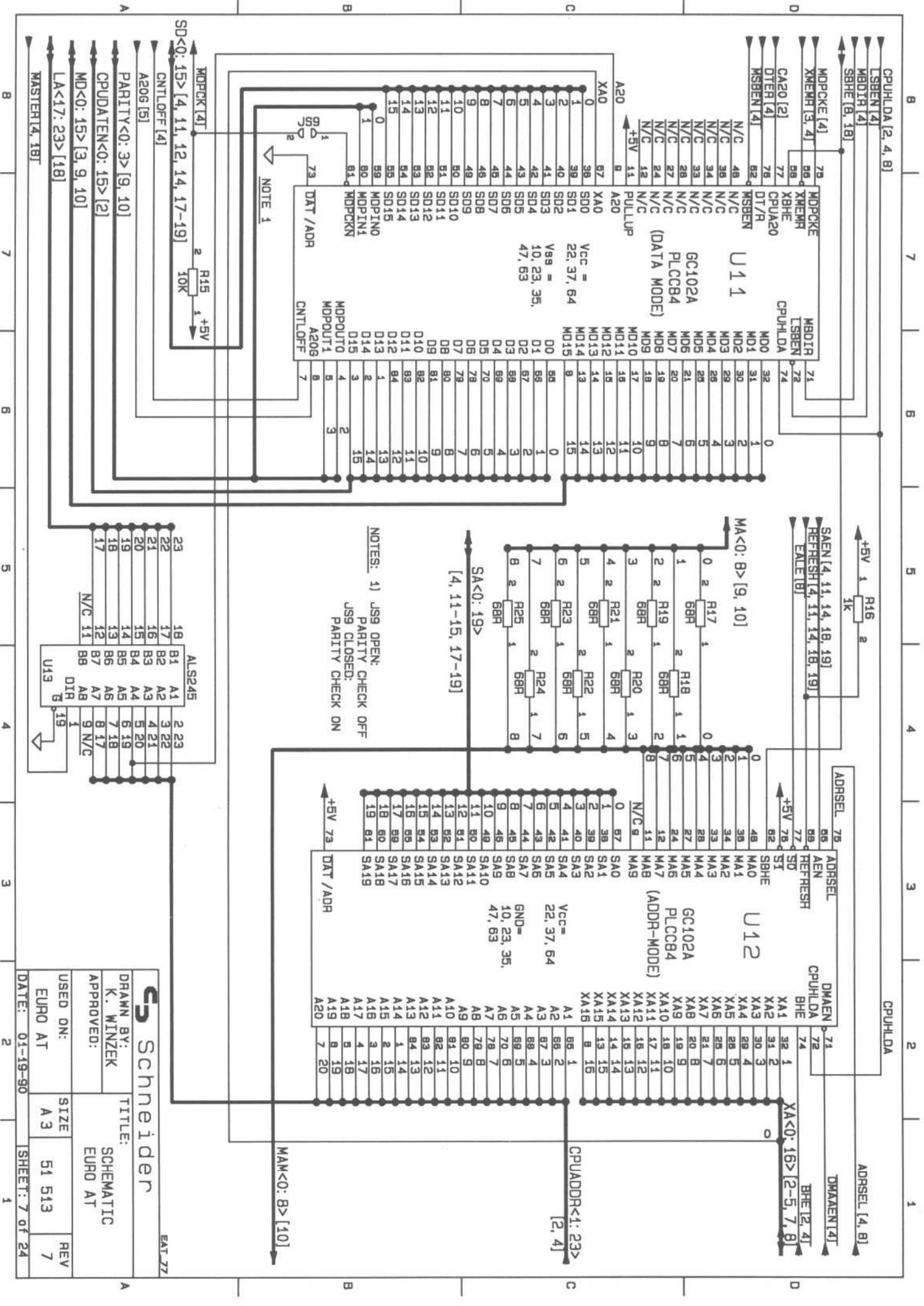
USED ON: SIZE A 3  
EURO AT 54 513  
DATE: 01-19-90 SHEET: 5 of 24



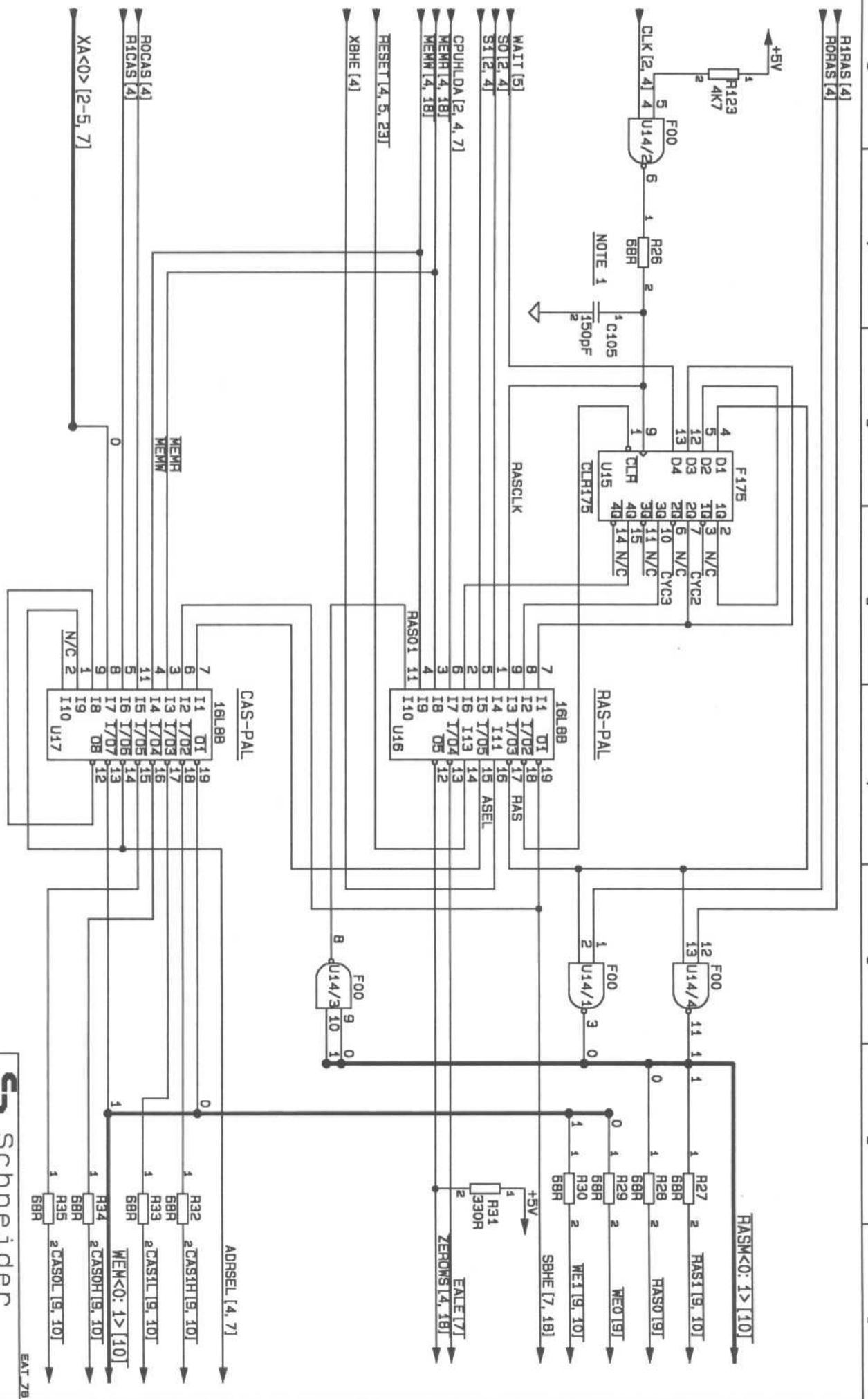
- NOTES
- 1) RCLR FUNCTION ONLY IN DS1287A
  - 2) C302 PLACED NEAR U10



		TITLE:		SCHEMATIC EURO AT
		DRAWN BY:		
K. WINZEK		APPROVED:		SIZE A3
USED ON: EURO AT		DATE: 01-19-90		
DATE: 01-19-90		SHEET: 8 of 24		EAT 28



		TITLE:	
		SCHEMATIC	
DRAWN BY:		K. WINZEK	
APPROVED:		EURO AT	
USED ON:	SIZE	REV	
EURO AT	A3	51 513	7
DATE: 01-19-90	SHEET: 7 of 24		



NOTES: 1) C105 NORMALLY NOT ASSEMBLED

**Schneider**

DATE: 01-18-90

REVISION: 7

SIZE: A3

SCHEMATIC EURO AT

EURO AT

51 513

8 of 24

DRAWN BY: K. WINZEK

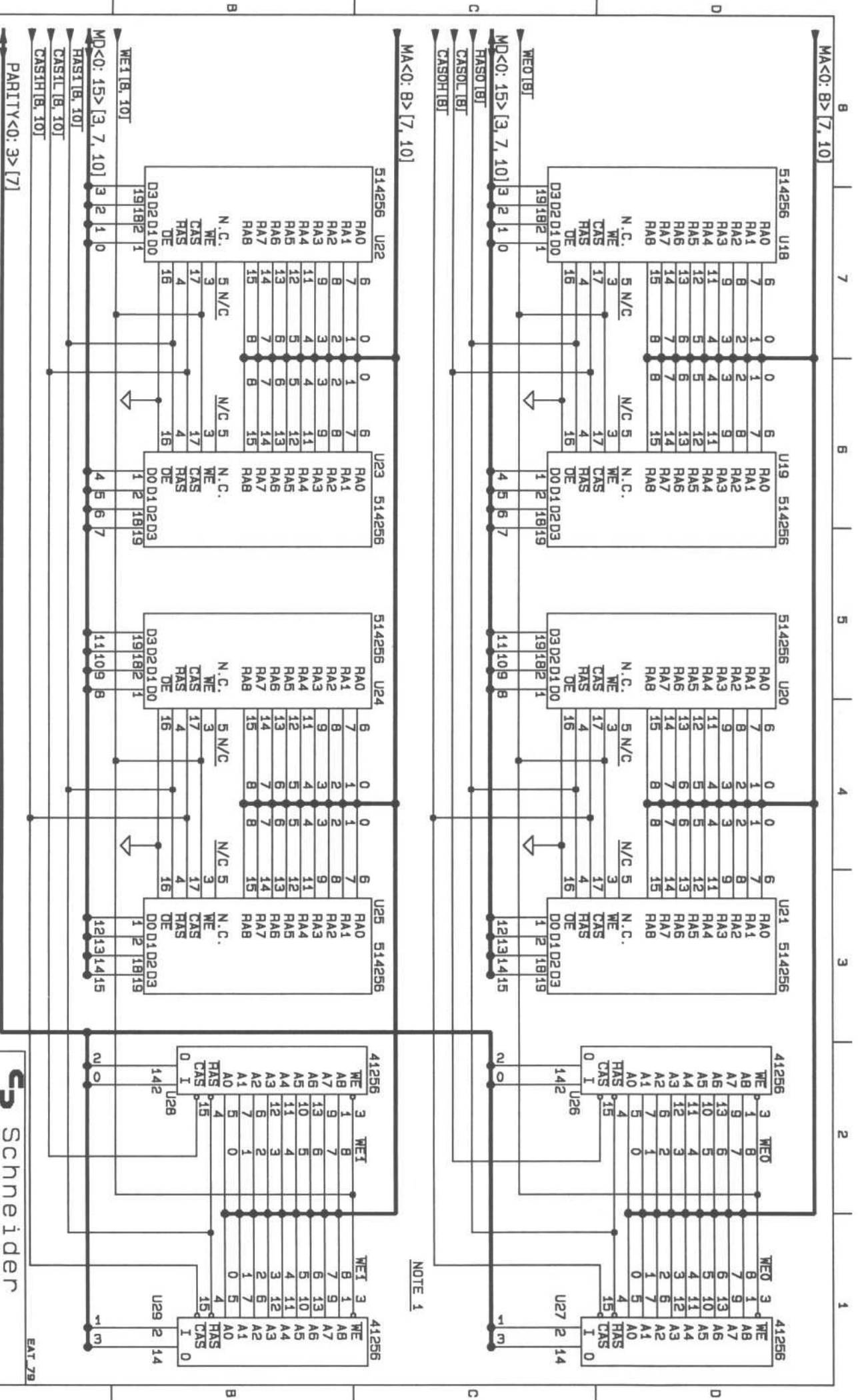
APPROVED:

TITLE: SCHEMATIC EURO AT

USED ON: EURO AT

DATE: 01-18-90

SHEET: 8 of 24



NOTES: 1) U26 .. U29 NORMALLY NOT ASSEMBLED

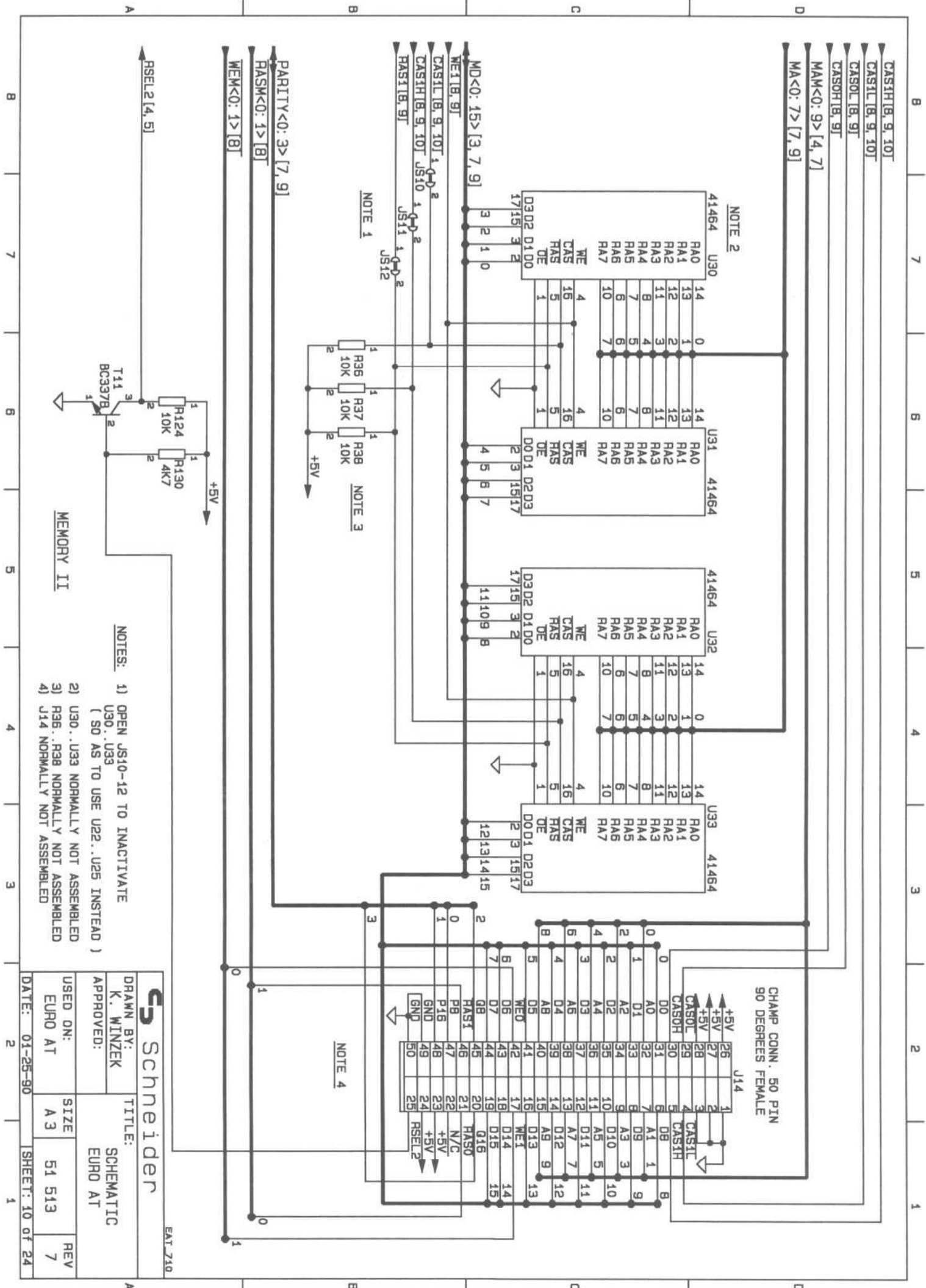
MEMORY I

**Schneider** EAT 79

DRAWN BY: K. WINZEK  
 APPROVED: EURO-AT  
 TITLE: SCHEMATIC EURO-AT  
 USED ON: EURO AT  
 SIZE: A 3  
 DATE: 01-18-90  
 SHEET: 9 of 24

REV 7





NOTE 2

NOTE 1

NOTE 3

NOTE 4

NOTES: 1) OPEN JS10-12 TO INACTIVATE U30..U33 (SO AS TO USE U22..U25 INSTEAD)

2) U30..U33 NORMALLY NOT ASSEMBLED  
 3) R36..R38 NORMALLY NOT ASSEMBLED  
 4) J14 NORMALLY NOT ASSEMBLED

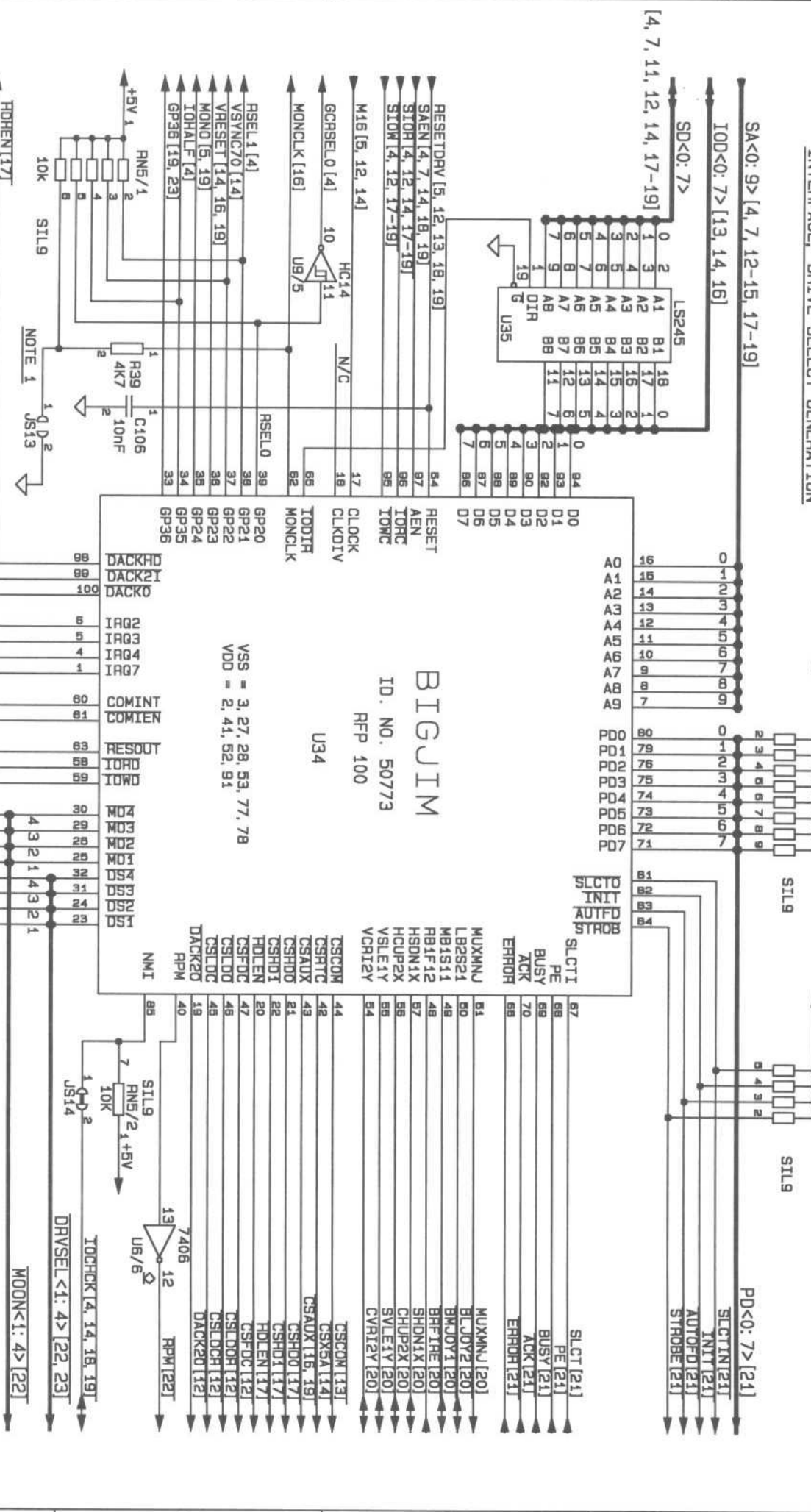
**Schneider**  
 EAT 240

DRAWN BY: K. WINZEK  
 APPROVED: SCHEMATIC  
 EURO AT

USED ON: SIZE A 3  
 EURO AT 51 513  
 REV 7

DATE: 01-25-90 SHEET: 10 of 24

I/O-CHIP SELECT GENERATION, JOYSTICK/MOUSE  
INTERFACE, DRIVE SELECT GENERATION



NOTES: 1) JS13 SELECTS BIGJIM  
BASE ADDRESS:  
OPEN: 25Xh, CLOSED: 35Xh

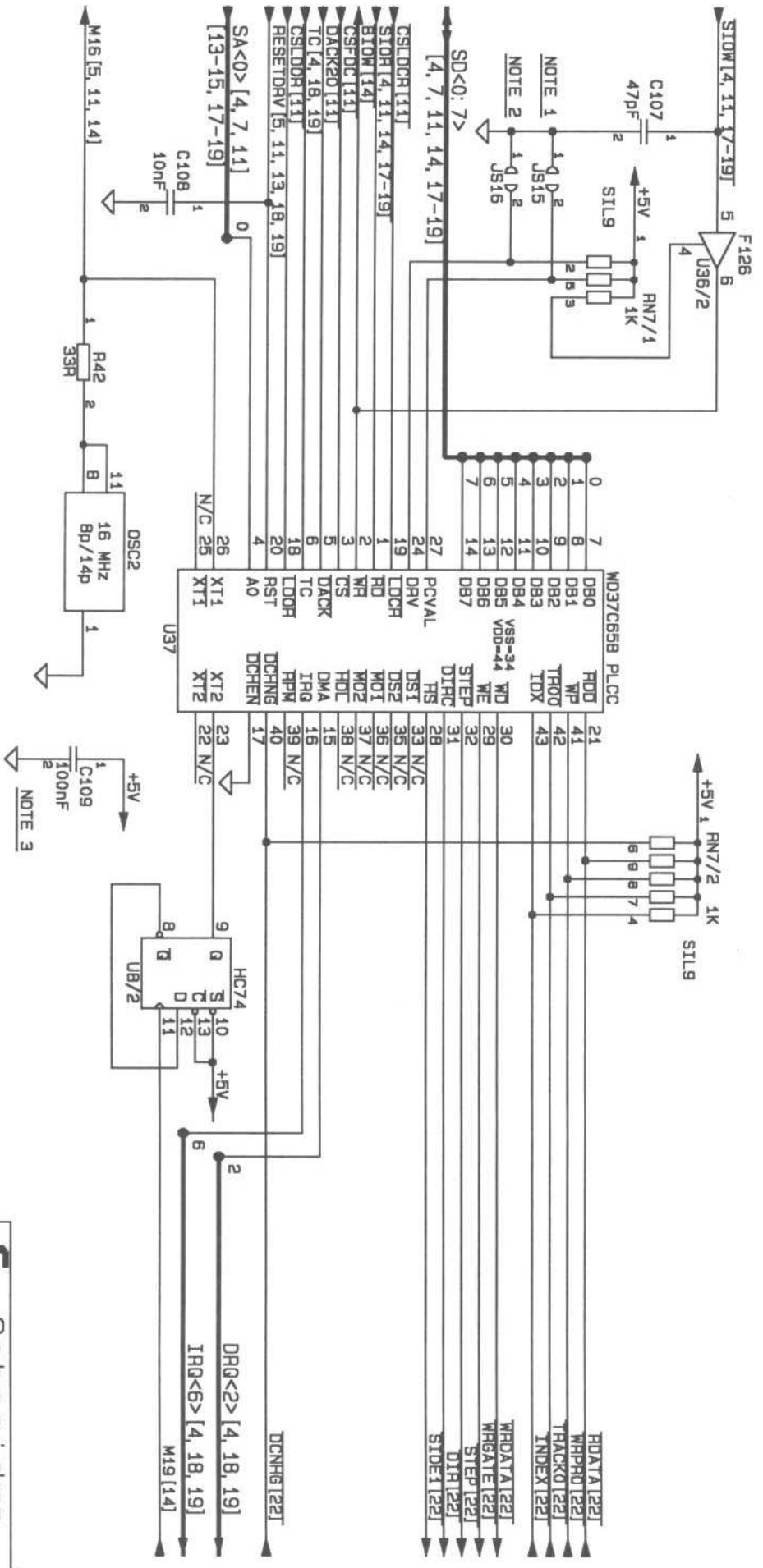
**Schneider**  
EAT 211

DRAWN BY: K. WINZEK  
TITLE: SCHEMATIC  
APPROVED: EURO-AT

USED ON: SIZE 51 513  
EURO AT A3 REV 7

DATE: 01-24-90	SHEET: 11 of 24
----------------	-----------------

FLOPPY INTERFACE



- NOTES:
- 1) JS15 SELECTS WRITE PRECOMP. VAL. :  
 JS15 OPEN : 125 ns  
 JS15 CLOSED: 187 ns
  - 2) JS16 SELECTS 2 SPEED SPINDLE DRIVE IF CLOSED
  - 3) C109 IS BLOCK CAP TO OSG2

**Schneider**  
EAT 212

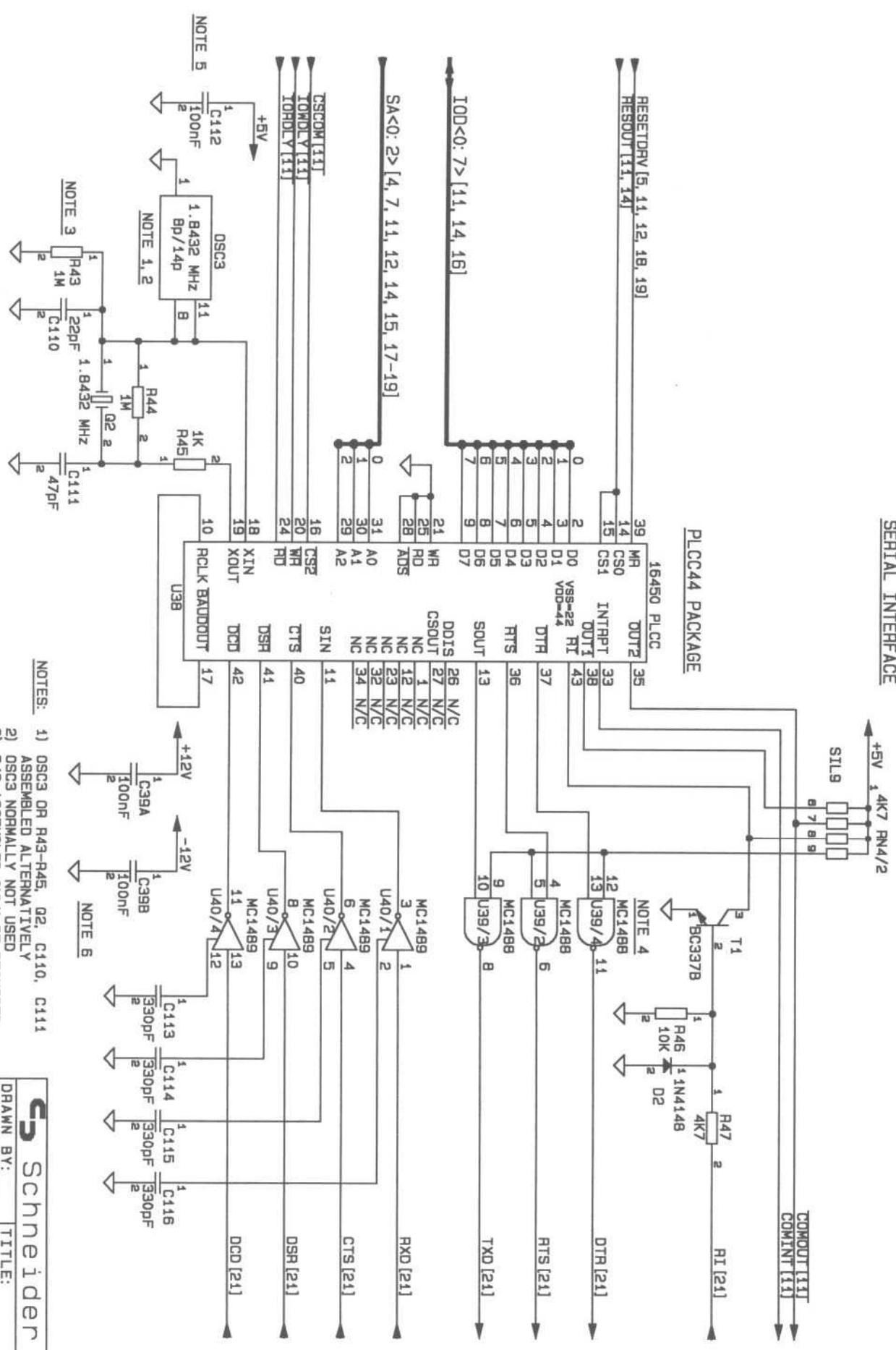
DRAWN BY: K. WINZEK  
 APPROVED: \_\_\_\_\_  
 TITLE: SCHEMATIC  
 EURO AT

USED ON: EURO AT  
 SIZE: A 3  
 REV: 7

DATE: 01-24-90  
 SHEET: 12 of 24

SERIAL INTERFACE

PLCC44 PACKAGE



NOTES:

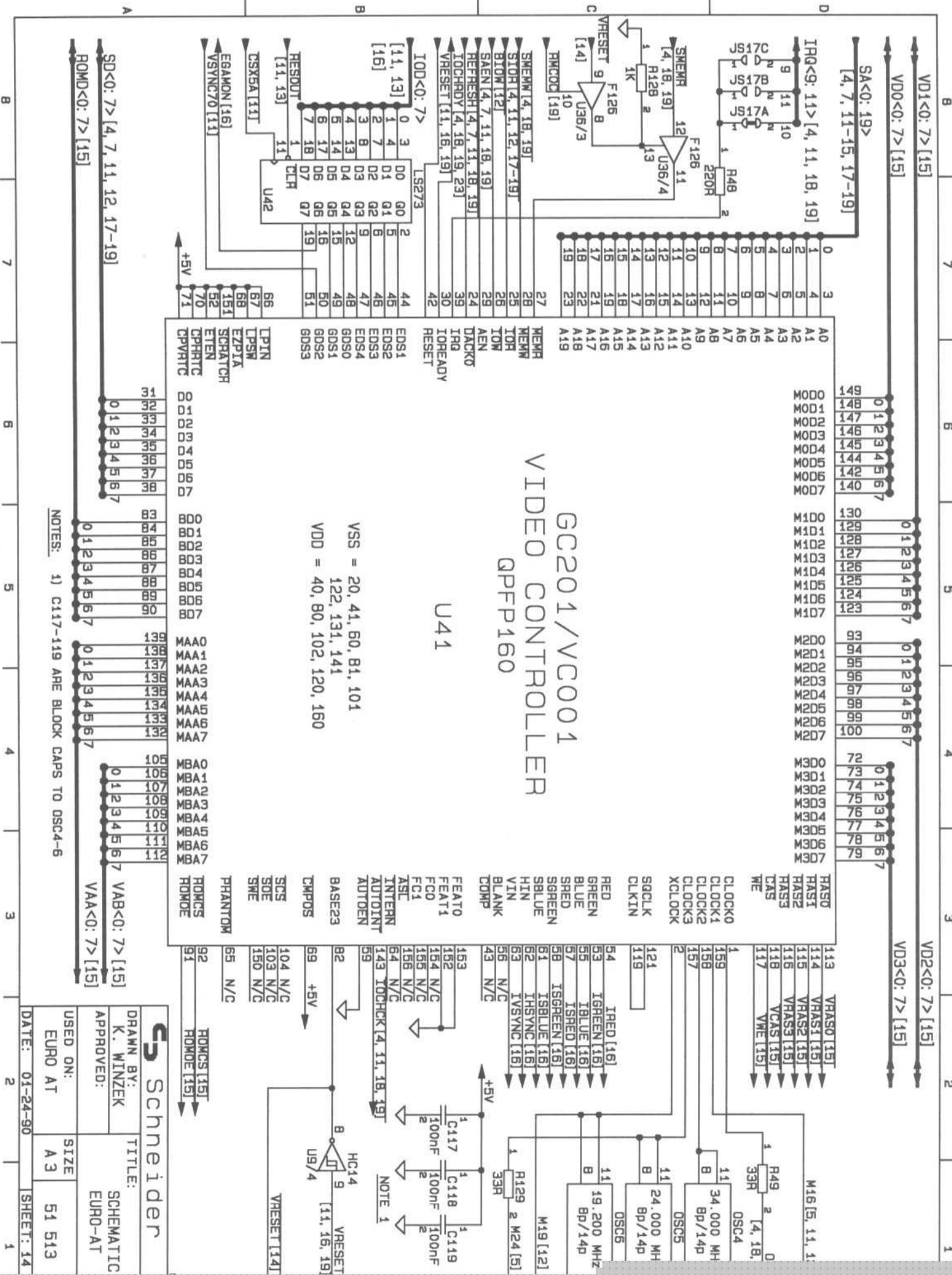
- 1) OSC3 OR R43-R45, Q2, C110, C111 ASSEMBLED ALTERNATIVELY
- 2) OSC3 NORMALLY NOT USED
- 3) R43 ASSEMBLED ONLY IF REQUIRED
- 4) U39 HAS SPECIAL POWER PINS:  
+12V = PIN 14  
-12V = PIN 1  
GND = PIN 7
- 5) C112 IS BLOCK CAP TO OSC3
- 6) C39A, C39B ARE BLOCK CAPS FOR U39



EAT 713

DRAWN BY: K. WINZEK  
 TITLE: SCHEMATIC  
 APPROVED: EURO-AT

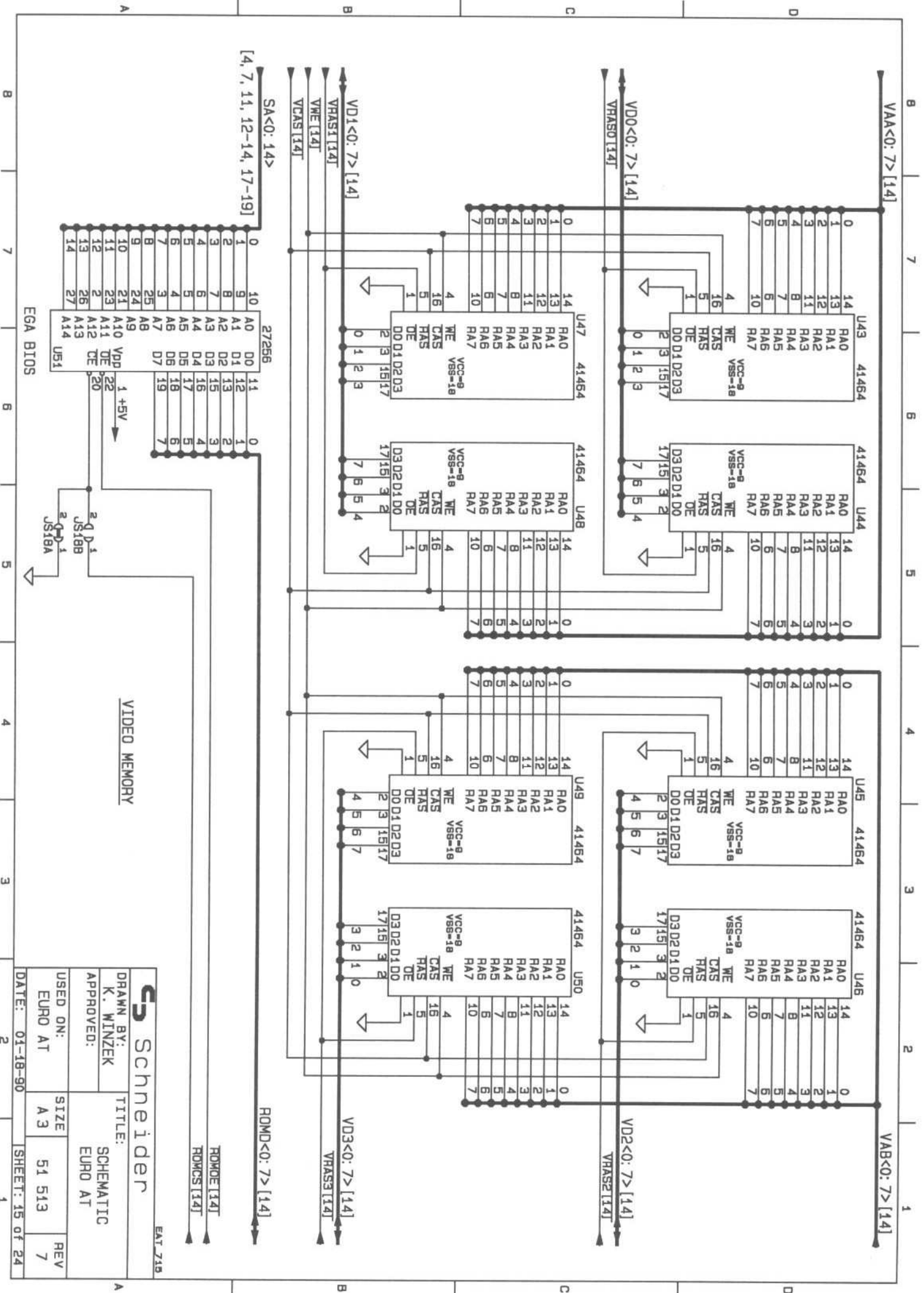
USED ON:	SIZE	REV
EURO AT	A 3	51 513 7
DATE: 01-18-90	SHEET: 13 of 24	



NOTES: 1) C117-119 ARE BLOCK CAPS TO OSC4-6

**Schneider**

DRAWN BY: K. WINZEK  
 APPROVED: [Signature]  
 USED ON: EURO AT  
 DATE: 01-24-90  
 TITLE: SCHEMATIC EURO-AT  
 SIZE: A3  
 SHEET: 14



VIDEO MEMORY

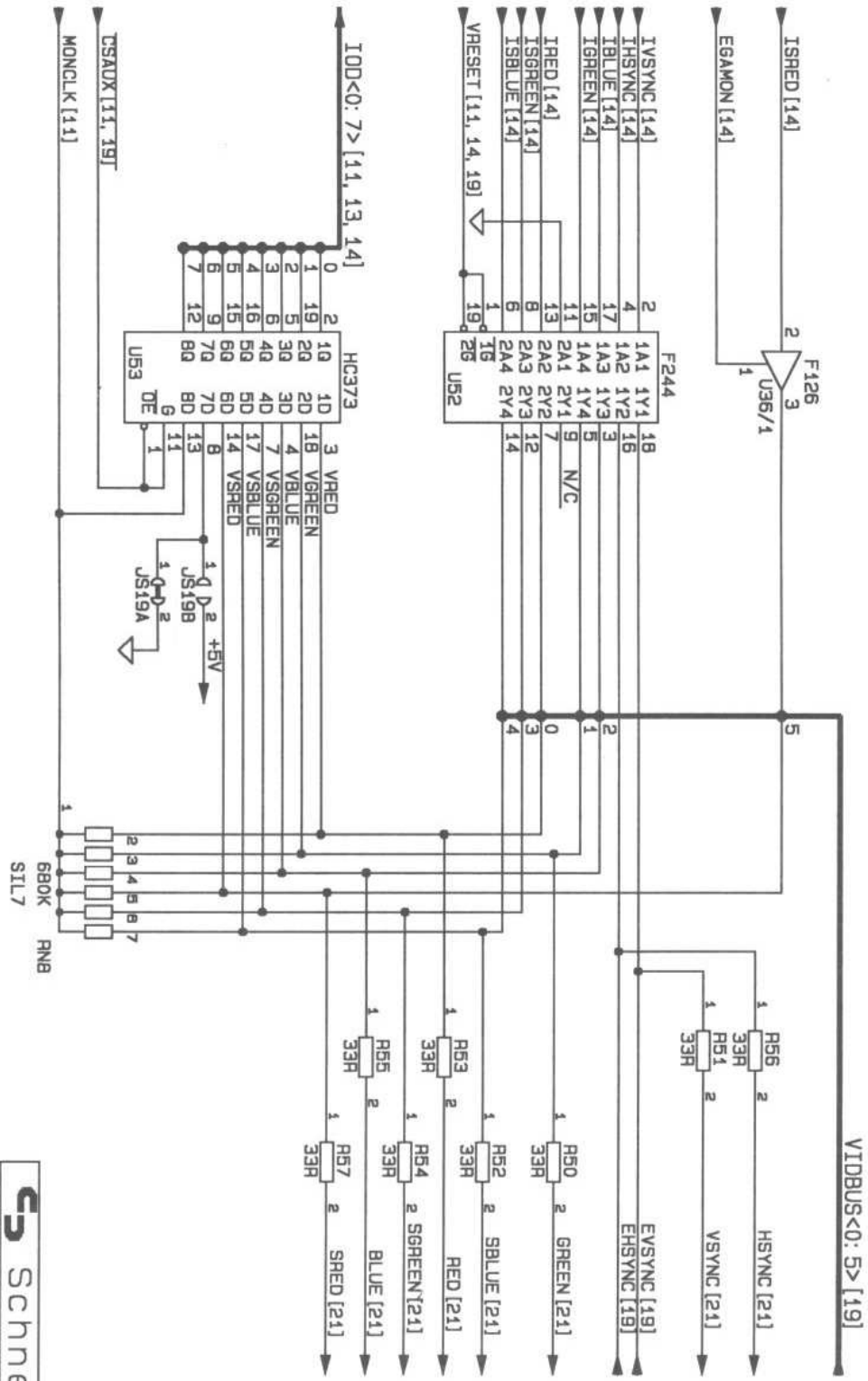
**Schneider**  
EAT 719

DRAWN BY: K. WINZEK  
APPROVED: SCHEMATIC  
EURO AT

USED ON: SIZE A 3  
EURO AT 51 513  
REV 7

DATE: 01-18-90 SHEET: 15 of 24

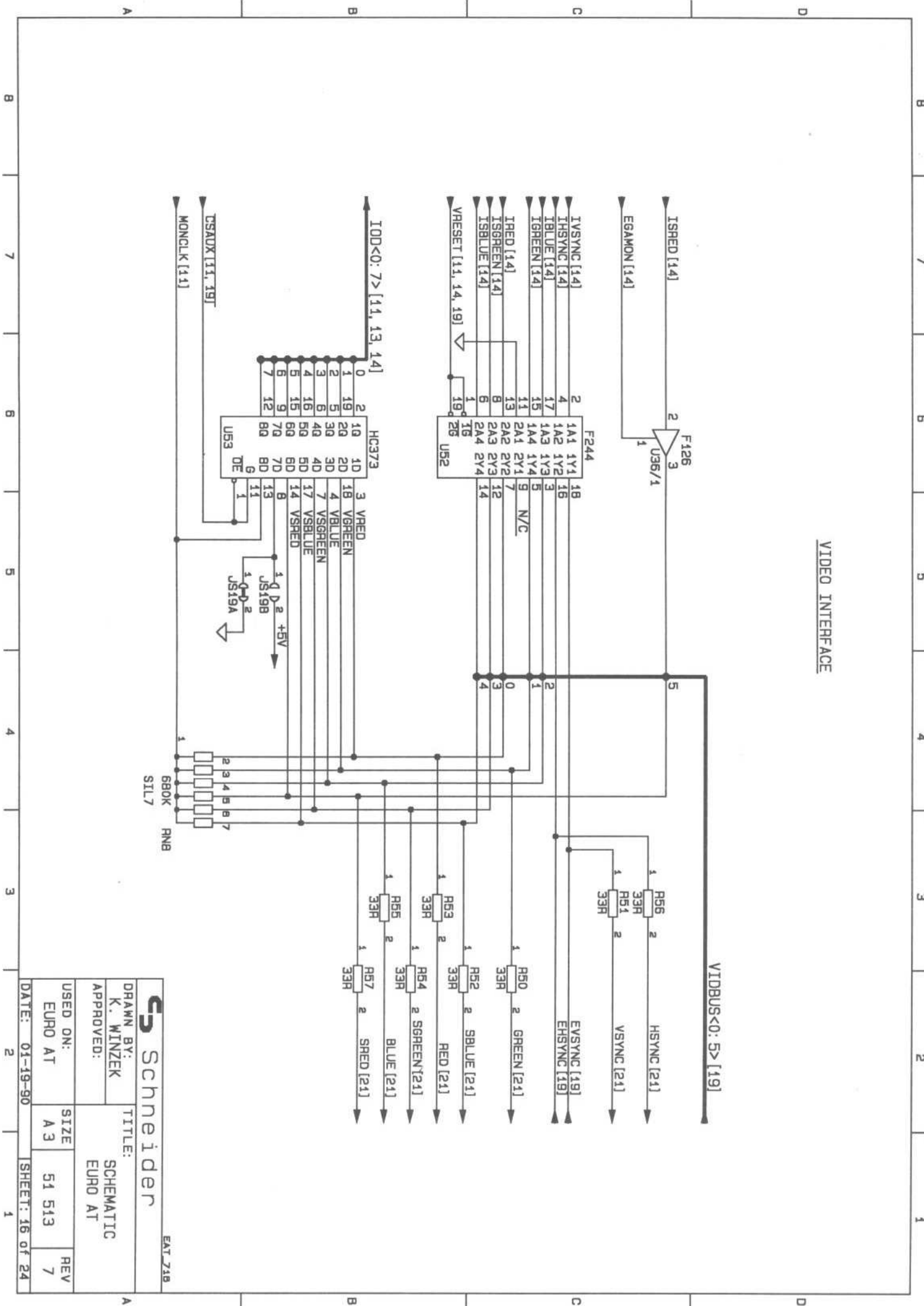
VIDEO INTERFACE

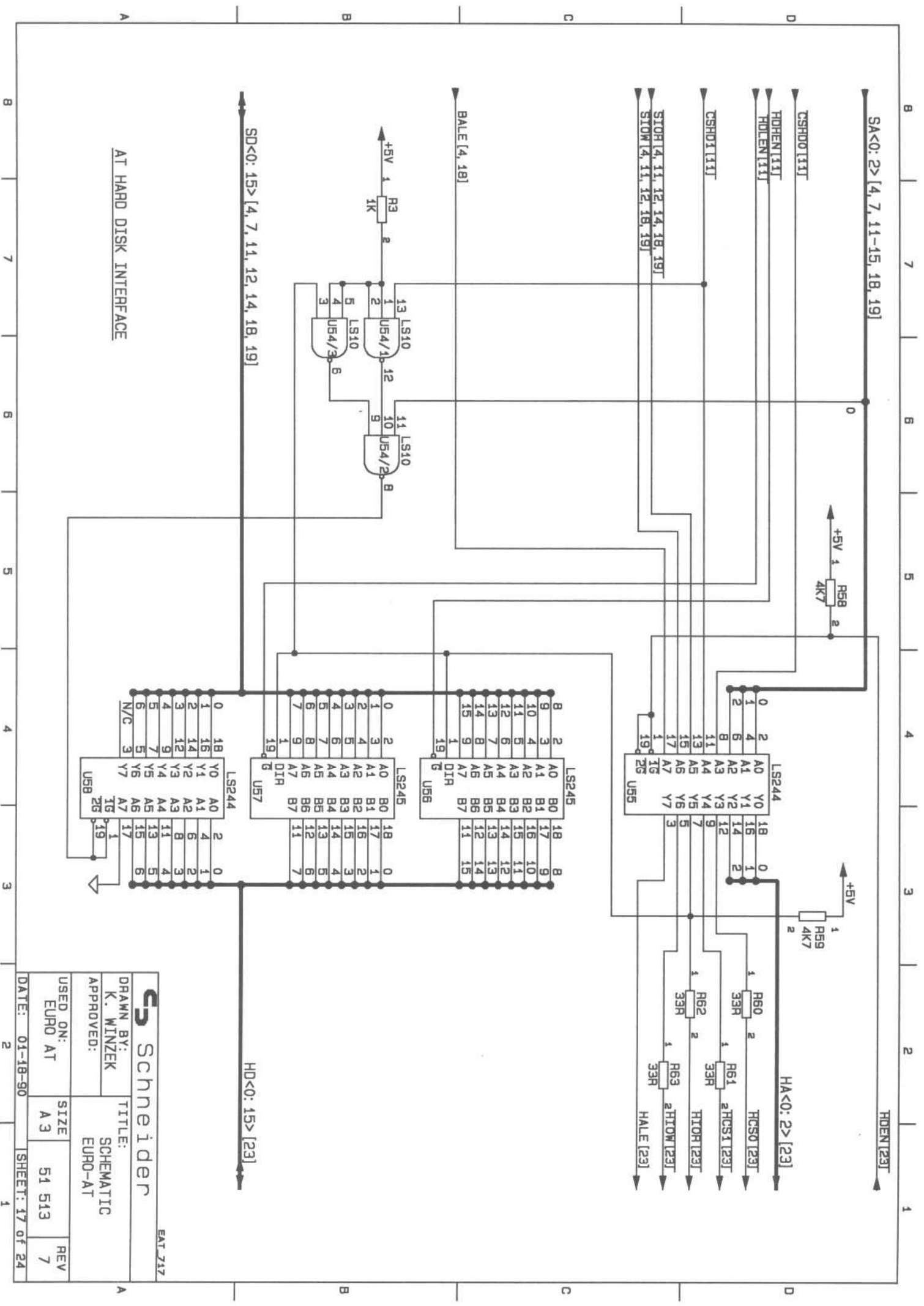


**Schneider** EAT 718

DRAWN BY: K. WINZEK  
 APPROVED: SCHEMATIC EURO AT

USED ON: SIZE A 3  
 EURO AT 51 513  
 DATE: 01-19-90 SHEET: 16 of 24





AT HARD DISK INTERFACE

**Schneider** EAT 217

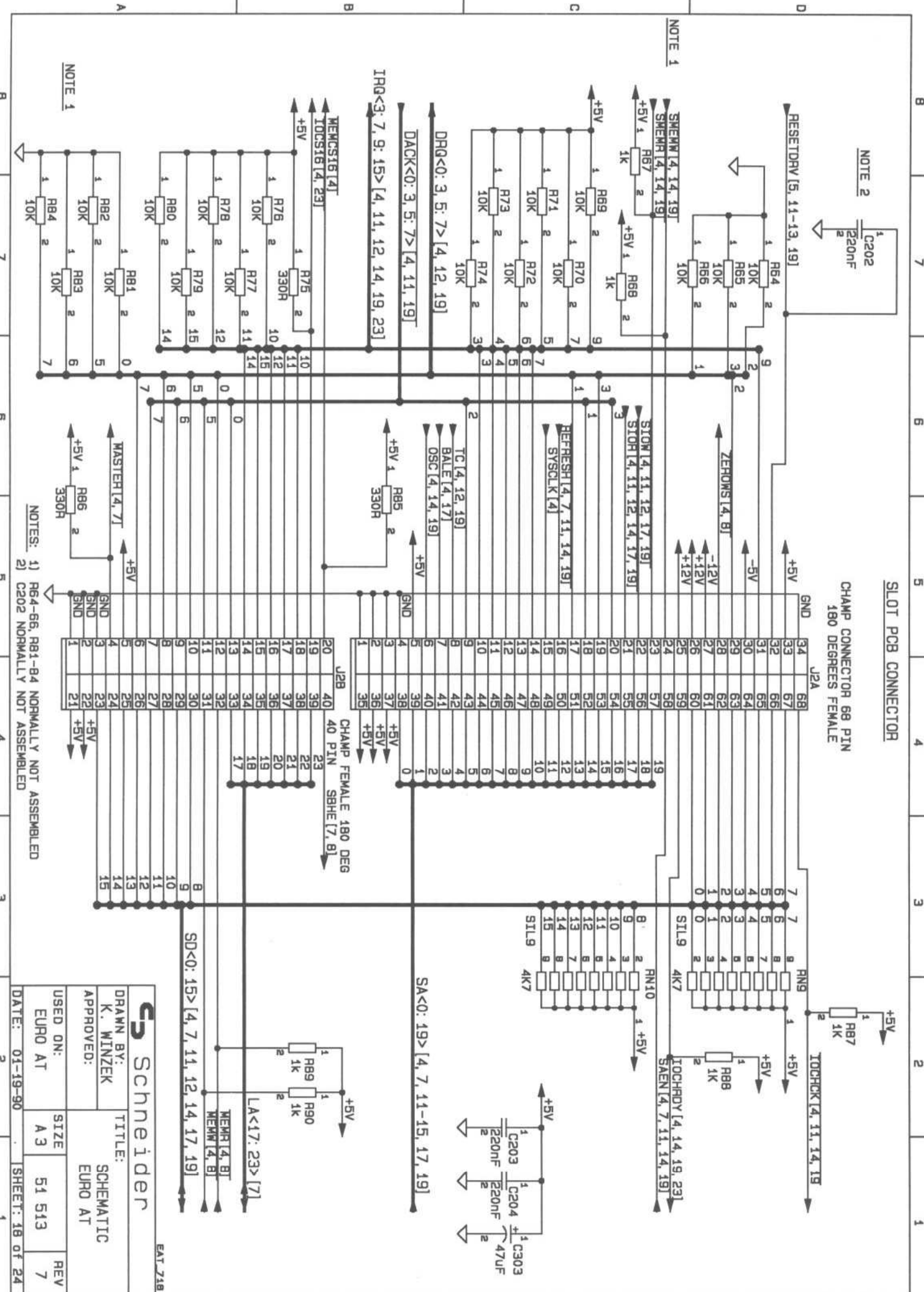
DRAWN BY: K. WINZEK  
 TITLE: SCHEMATIC EURO-AT

APPROVED:

USED ON: EURO AT  
 SIZE: A3  
 REV: 7

DATE: 01-18-90  
 SHEET: 17 of 24





NOTE 2  
C202  
220nF

RESETPRV [5, 11-13, 19]

NOTE 1

SMEM1[4, 14, 19]  
SHEM1[4, 14, 19]

DRQ<0: 3, 5: 7> [4, 11, 19]  
DRQ<3: 7, 9: 15> [4, 11, 12, 14, 19, 23]

MEMCS16[4]  
IOCS16[4, 23]

NOTE 1  
R64, R65, R66, R67, R68, R69, R70, R71, R72, R73, R74, R75, R76, R77, R78, R79, R80, R81, R82, R83, R84

SLOT PCB CONNECTOR  
CHAMP CONNECTOR 68 PIN  
180 DEGREES FEMALE

J2A

34 68  
33 67  
32 66  
31 65  
30 64  
29 63  
28 62  
27 61  
26 60  
25 59  
24 58  
23 57  
22 56  
21 55  
20 54  
19 53  
18 52  
17 51  
16 50  
15 49  
14 48  
13 47  
12 46  
11 45  
10 44  
9 43  
8 42  
7 41  
6 40  
5 39  
4 38  
3 37  
2 36  
1 35

J2B

20 40  
19 39  
18 38  
17 37  
16 36  
15 35  
14 34  
13 33  
12 32  
11 31  
10 30  
9 29  
8 28  
7 27  
6 26  
5 25  
4 24  
3 23  
2 22  
1 21

CHAMP FEMALE 180 DEG  
40 PIN SBHE [7, 8]

SA<0: 19> [4, 7, 11-15, 17, 19]

LA<17: 23> [7]

SD<0: 15> [4, 7, 11, 12, 14, 17, 19]

MEM1[4, 8]  
MEM2[4, 8]

TOCHCK [4, 11, 14, 19]  
TOCHRDY [4, 14, 19, 23]  
SAEN [4, 7, 11, 14, 19]

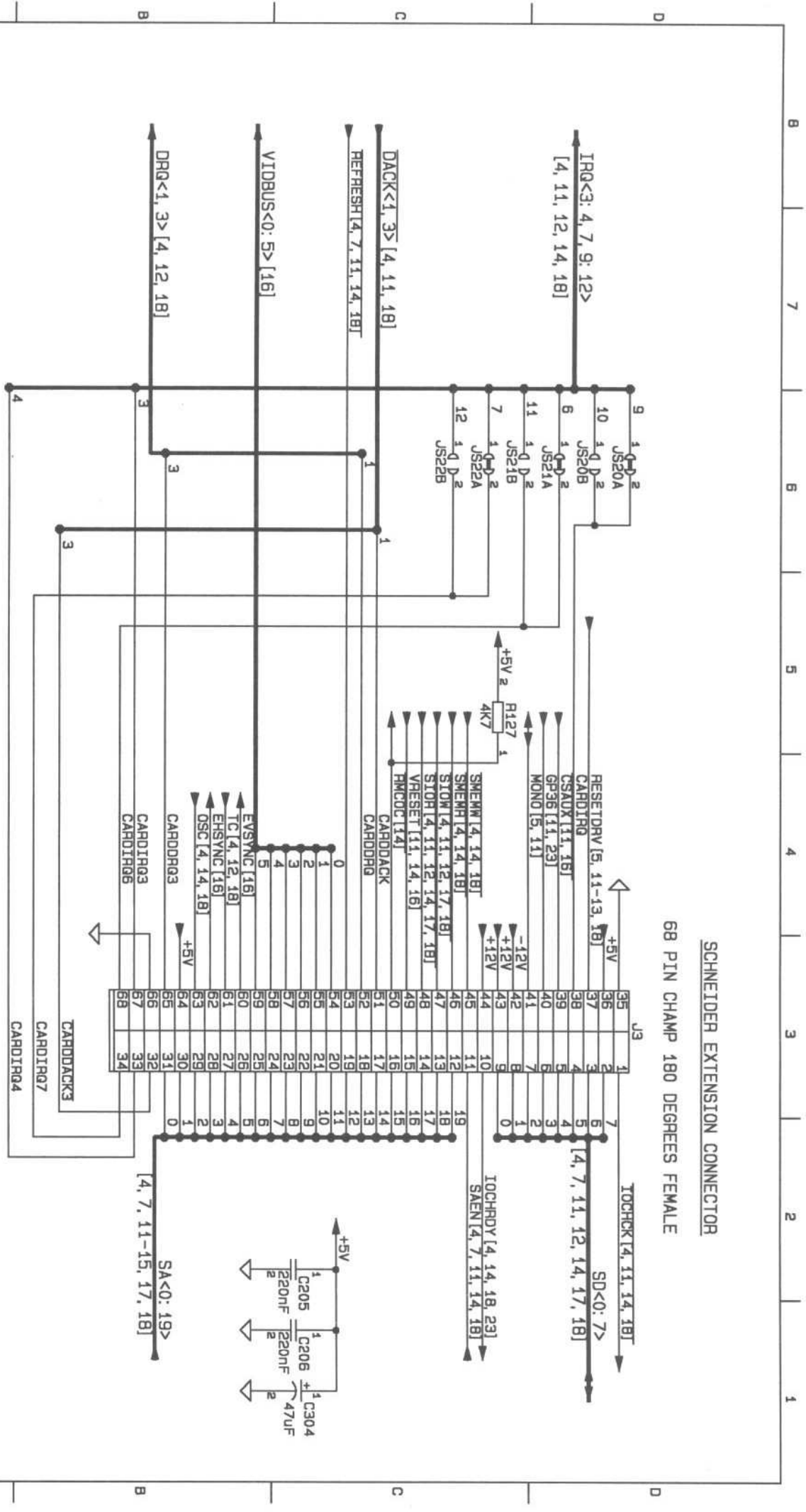
C203 220nF  
C204 220nF  
C303 47uF

Schneider

EAT 718

DRAWN BY: K. WINZKE	TITLE: SCHEMATIC
APPROVED:	EURO AT
USED ON: EURO AT	SIZE A3
DATE: 01-19-90	REV 7
	SHEET: 18 of 24

**SCHNEIDER EXTENSION CONNECTOR**  
**68 PIN CHAMP 180 DEGREES FEMALE**

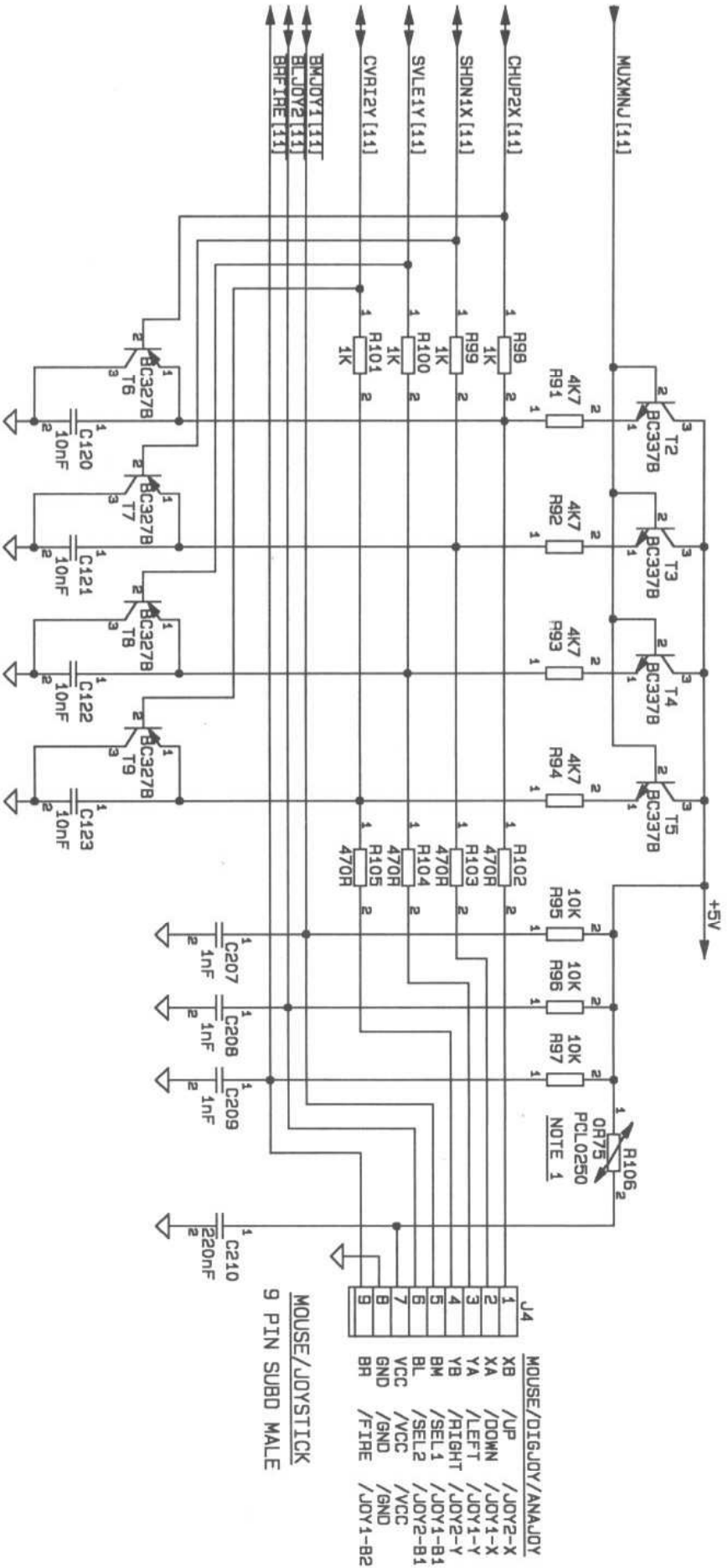


**Schneider**  
EAT 719

DRAWN BY: K. WINZEK APPROVED: USED ON: EURO AT	TITLE: SCHEMATIC EURO AT	SIZE A3	REV 7
--	--------------------------------	------------	----------

DATE: 01-18-90 SHEET: 19 of 24

JOYSTICK/MOUSE CONNECTOR



9 PIN SUBD MALE  
 MOUSE/JOYSTICK

J4		MOUSE/DIGJOY/ANAJDY	
1	XB	/UP	/JOY2-X
2	XA	/DOWN	/JOY1-X
3	YA	/LEFT	/JOY1-Y
4	YB	/RIGHT	/JOY2-Y
5	BM	/SEL1	/JOY1-B1
6	BL	/SEL2	/JOY2-B1
7	VCC	/VCC	/VCC
8	GND	/GND	/GND
9	BR	/FIRE	/JOY1-B2

CONNECTORS I

**Schneider**

EAT 720

DRAWN BY:  
K. WINZEK

TITLE:  
SCHEMATIC

APPROVED:

EURO-AT

USED ON:  
EURO AT

SIZE  
A 3

DATE: 01-18-90

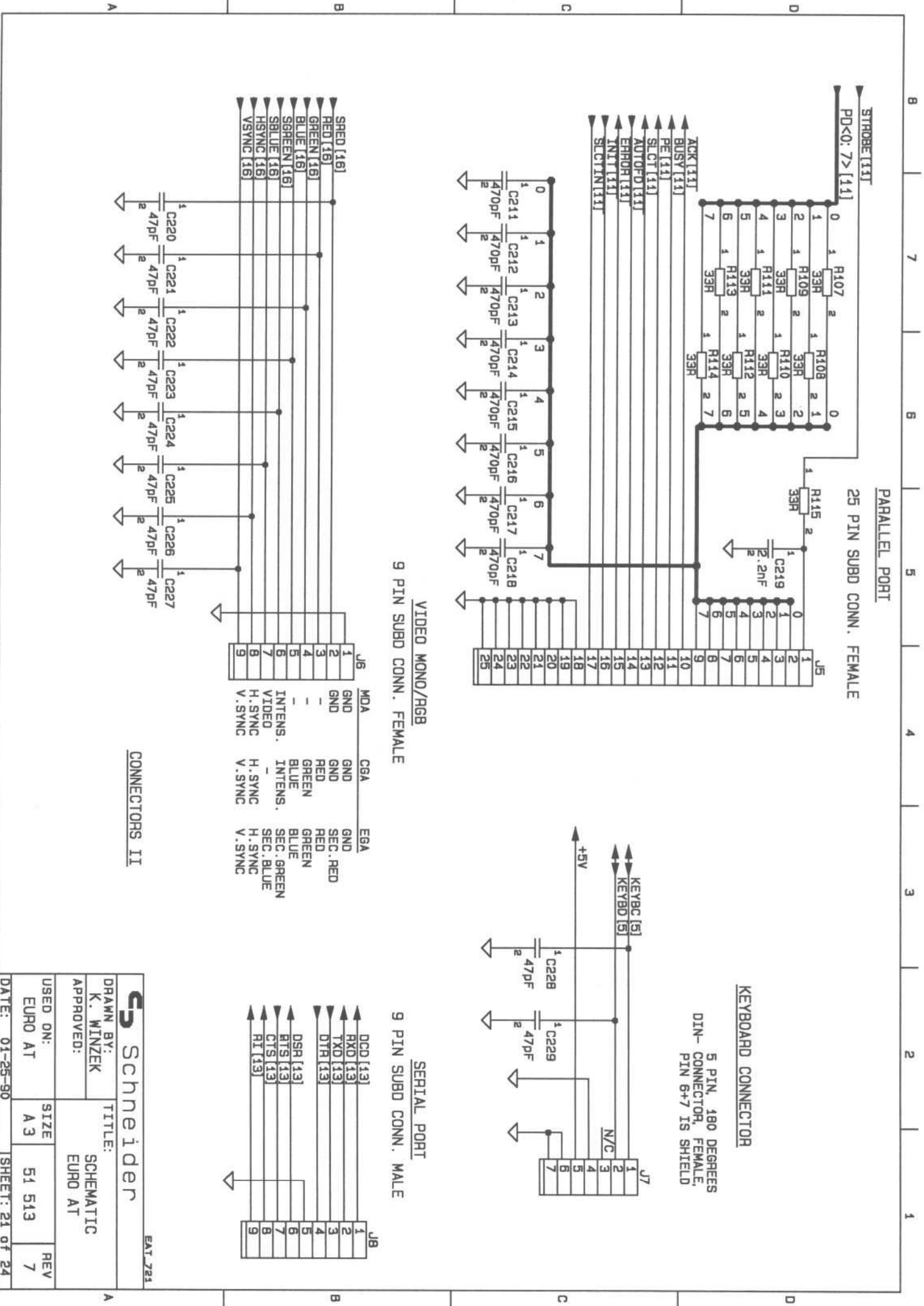
51 513

DATE: 01-18-90

REV  
7

NOTES: 1) R106 IS A PTC RESISTOR FOR CURRENT LIMITATION

8 7 6 5 4 3 2 1

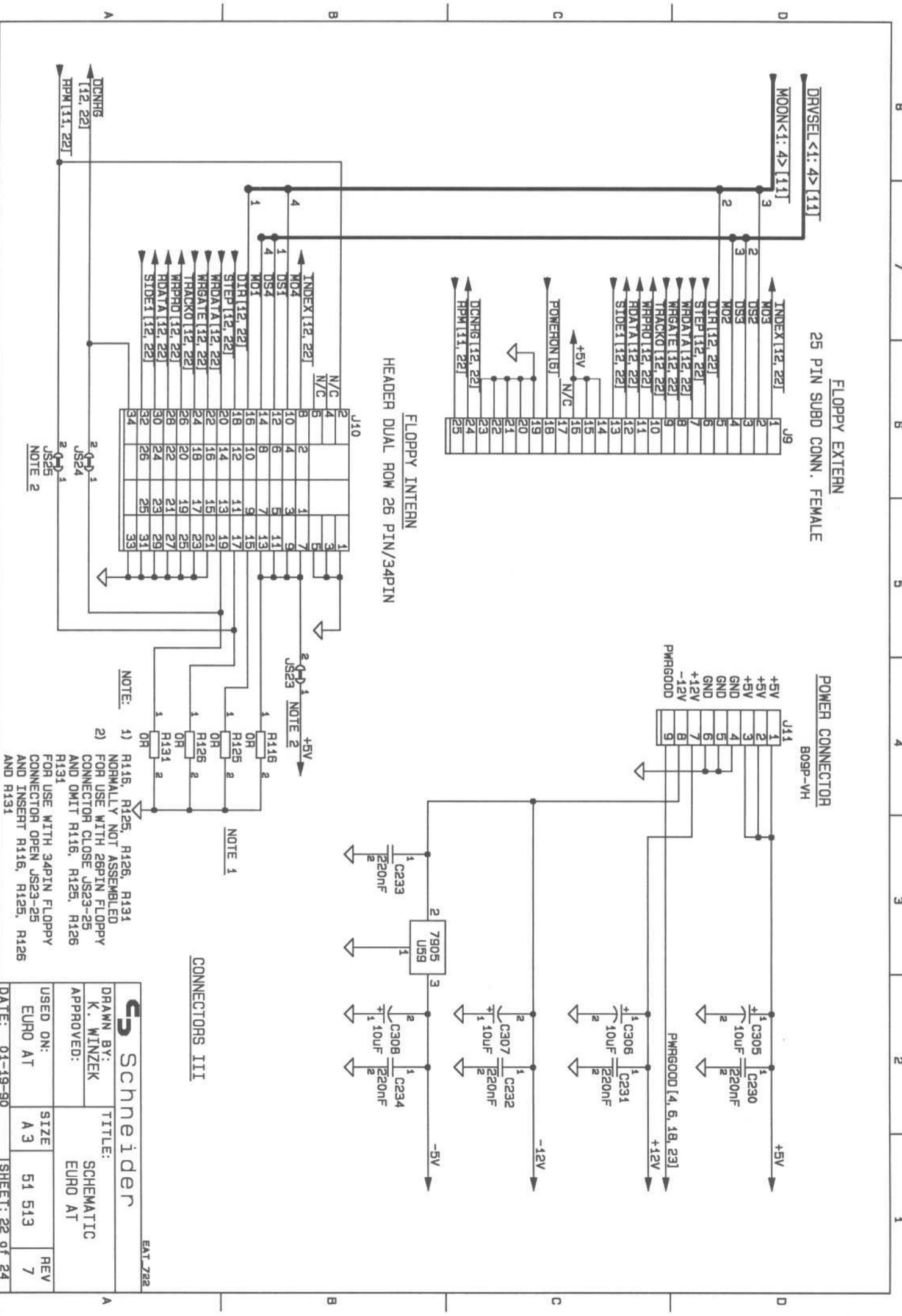


**Schneider** EAT 724

DRAWN BY: K. WINZEK  
 TITLE: SCHEMATIC  
 APPROVED: EURO AT

USED ON: SIZE A 3  
 EURO AT 51 513  
 REV 7

DATE: 01-25-90 SHEET: 21 of 24



**Schneider**  
 DRAWN BY: K. WINZEK  
 APPROVED: [Signature]  
 TITLE: SCHEMATIC EURO AT  
 USED ON: EURO AT  
 DATE: 01-19-90  
 SHEET: 22 of 24

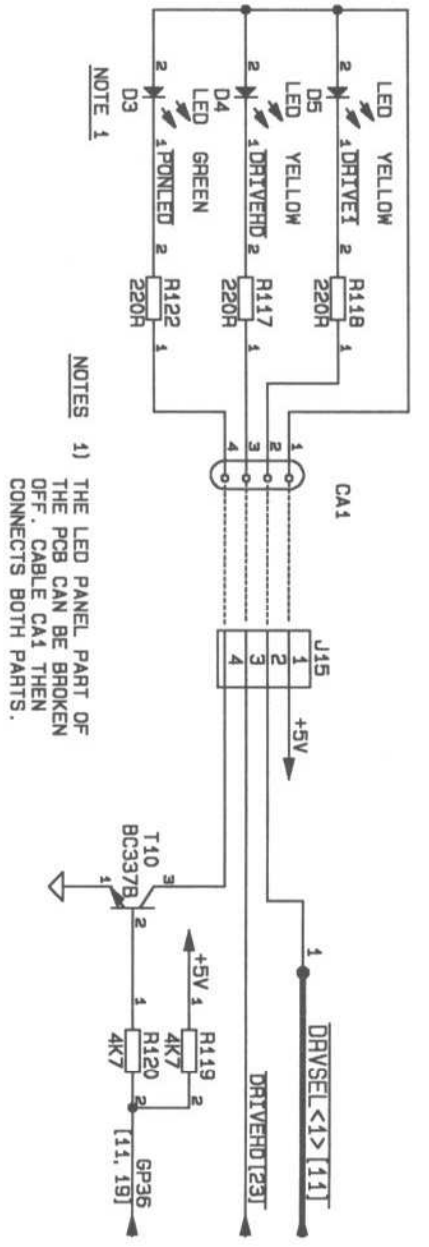
8 7 6 5 4 3 2 1

CONNECTORS IV / LED PANEL / SPARE GATES

RESET



LED PANEL



NOTE 1

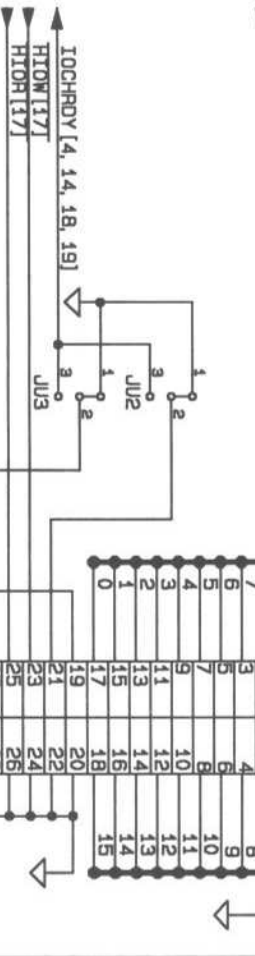
NOTES 1) THE LED PANEL PART OF THE PCB CAN BE BROKEN OFF. CABLE CA1 THEN CONNECTS BOTH PARTS.

AT INTEGRATED HARD DISK DRIVE CONNECTOR

HCS1171

HD<0: 15> [17]

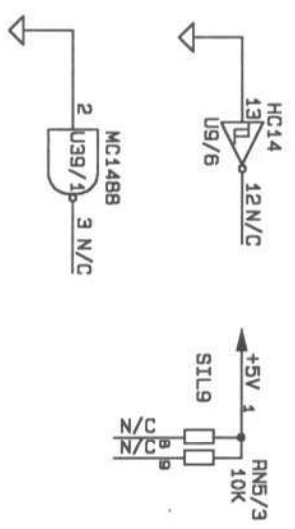
RESET [4, 5, 8]



40 PIN HEADER DUAL ROW

SPARE GATES

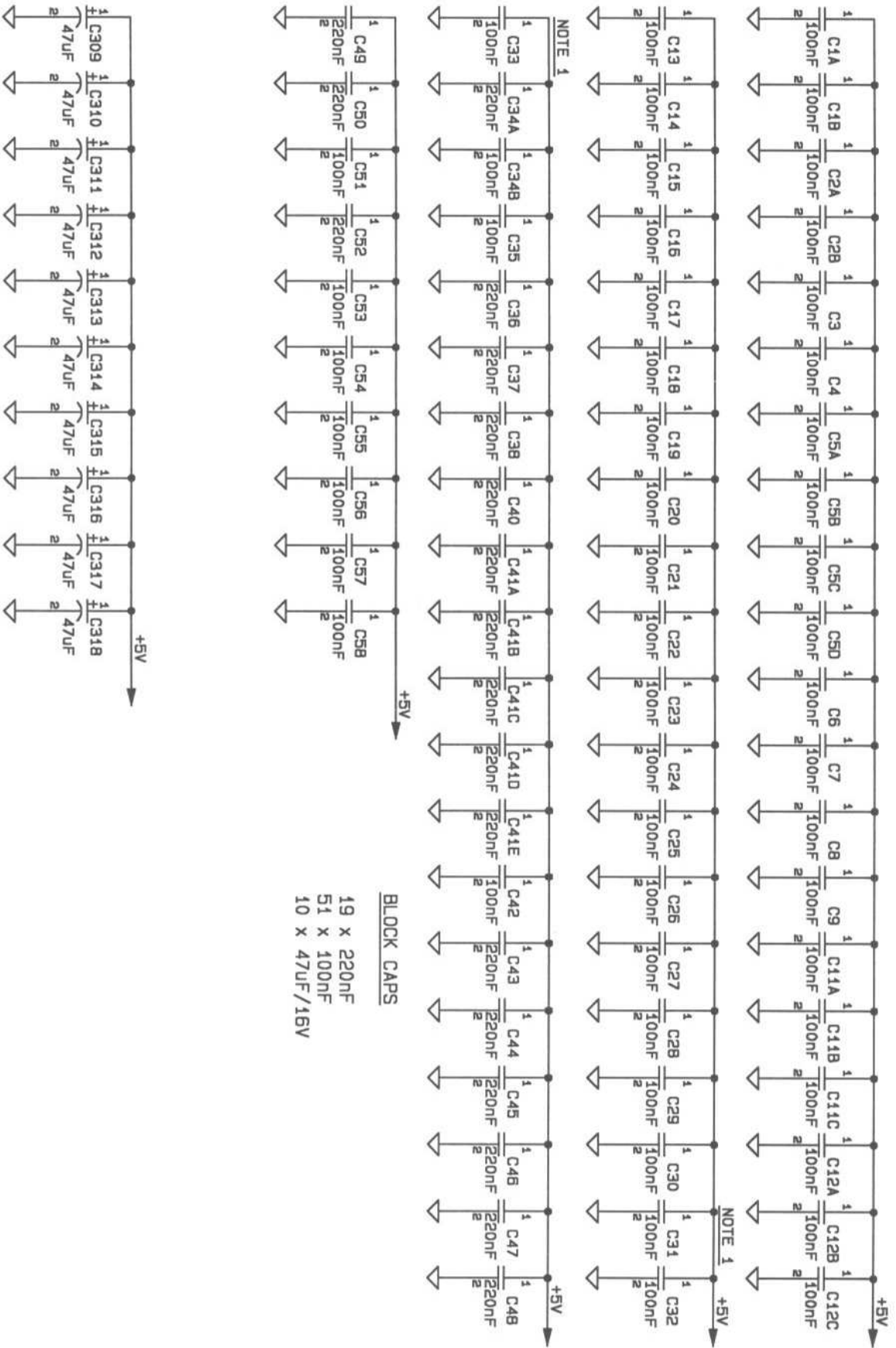
SPARE R-NETS



**Schneider**

DRAWN BY: K. WINZEK  
 APPROVED: SCHEMATIC EURO AT

USED ON:	SIZE	REV
EURO AT	A 3	51 513 7
DATE: 01-25-90	SHEET: 23 of 24	



NOTES: 1) C30..C33 NORMALLY NOT ASSEMBLED

BLOCK CAPS  
 19 X 220NF  
 51 X 100NF  
 10 X 47UF/16V

**Schneider**

EAT 724

DRAWN BY: K. WINZEK  
 APPROVED: \_\_\_\_\_  
 TITLE: SCHEMATIC EURO AT

USED ON: EURO AT  
 SIZE: A3  
 REV: 7

DATE: 01-19-90  
 SHEET: 24 of 24

REVISIONS

LTR	DESCRIPTION	DATE	APPROVED
0	FIRST DRAWING	07-31-89	K. WINZEK
1	PREPRODUCTION RELEASE	11-23-89	K. WINZEK
2	NEW G207	01-12-90	K. WINZEK

DRAWING FILES:

SLT\_21  
 SLT\_22  
 SLT\_23  
 SLT\_24

CONTENTS:

THIS SHEET  
 SLOT PCB CONNECTOR  
 SLOT I  
 SLOT II



SLT\_21

DRAWN BY: K. WINZEK  
 APPROVED:  
 TITLE: SCHEMATIC SLOTS EURO AT

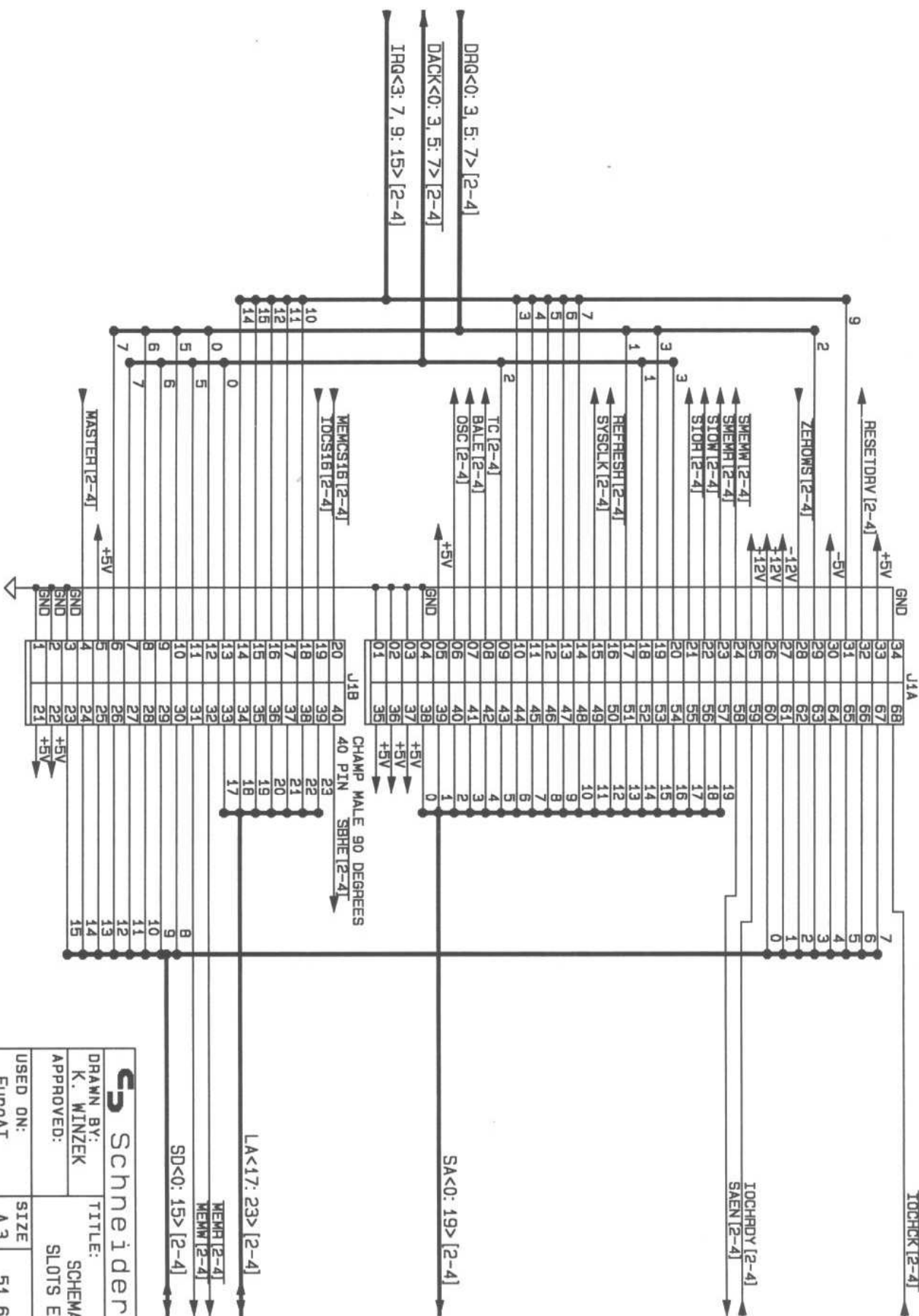
USED ON:	SIZE	REV
EUPDAT	A3	2

DATE: 01-29-90 SHEET: 1 of 4



SLOT PCB CONNECTOR

CHAMP CONNECTOR 68 PIN  
90 DEGREES MALE

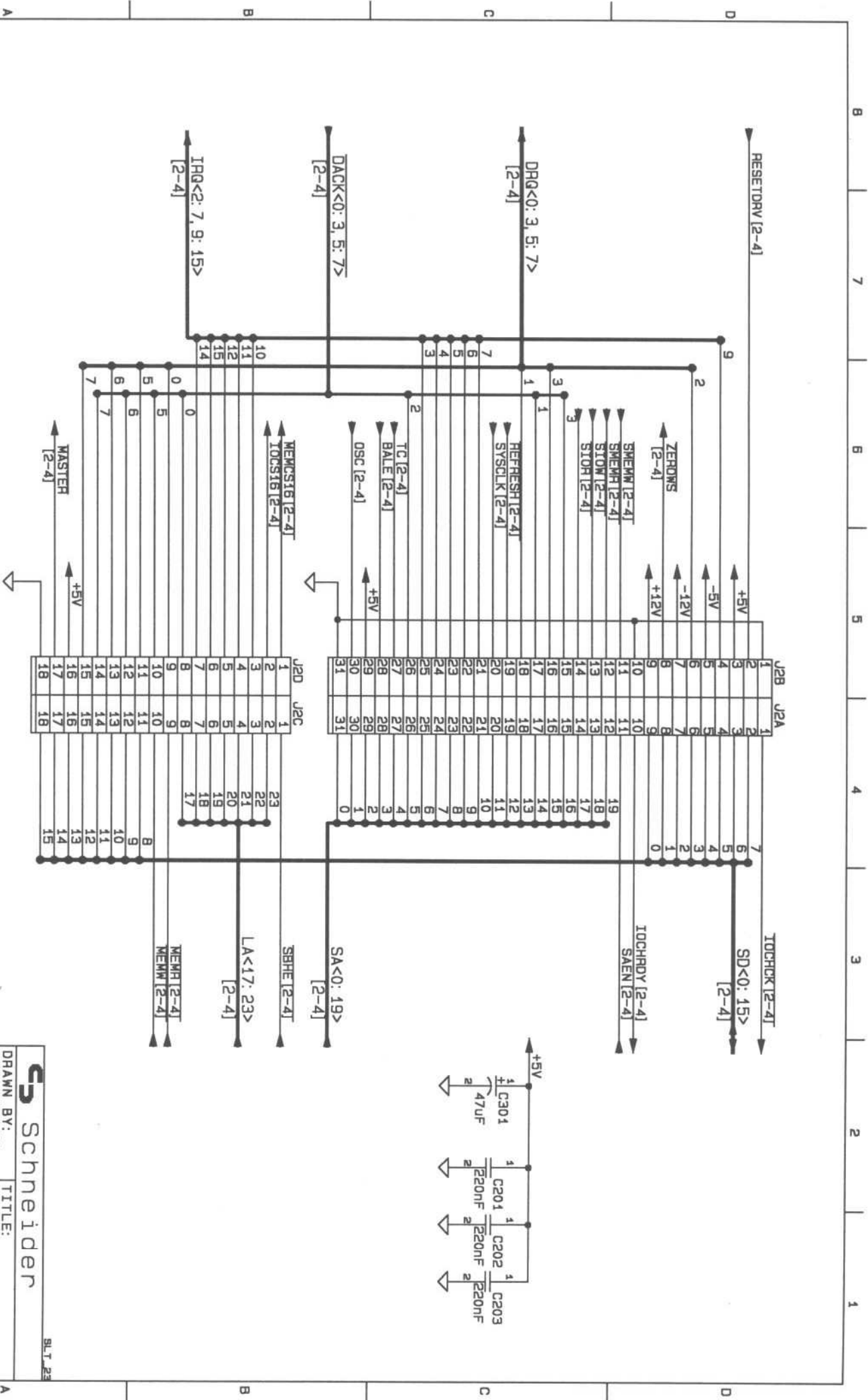


Schneider

TITLE: SCHEMATIC SLOTS EURO AT

DRAWN BY: K. WINZEK  
 APPROVED: [Signature]  
 USED ON: SIZE A3  
 EURO AT 51 620  
 DATE: 01-29-90 SHEET: 2 of 4

REV 2

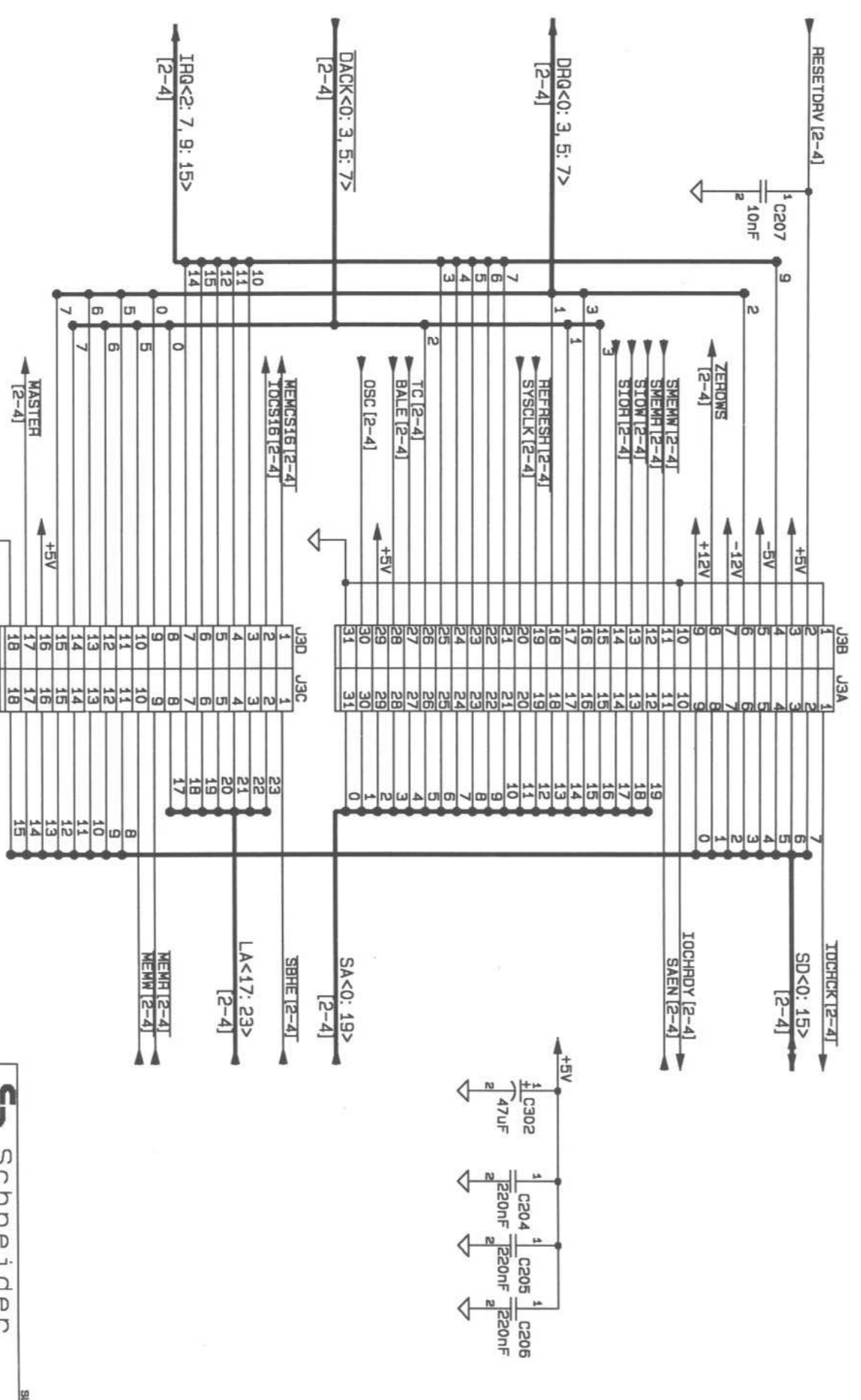


SLOT 1



SLT 23

DRAWN BY: K. WINZEK		TITLE: SCHEMATIC	
APPROVED:		SLOTS EURO AT	
USED ON: EURO AT	SIZE A3	51 620	REV 2
DATE: 01-29-90	SHEET: 3 of 4		



NOTE 1

J30	J3C
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16
17	17
18	18

NOTES: 1) J3C, J3D  
NORMALLY NOT  
ASSEMBLED

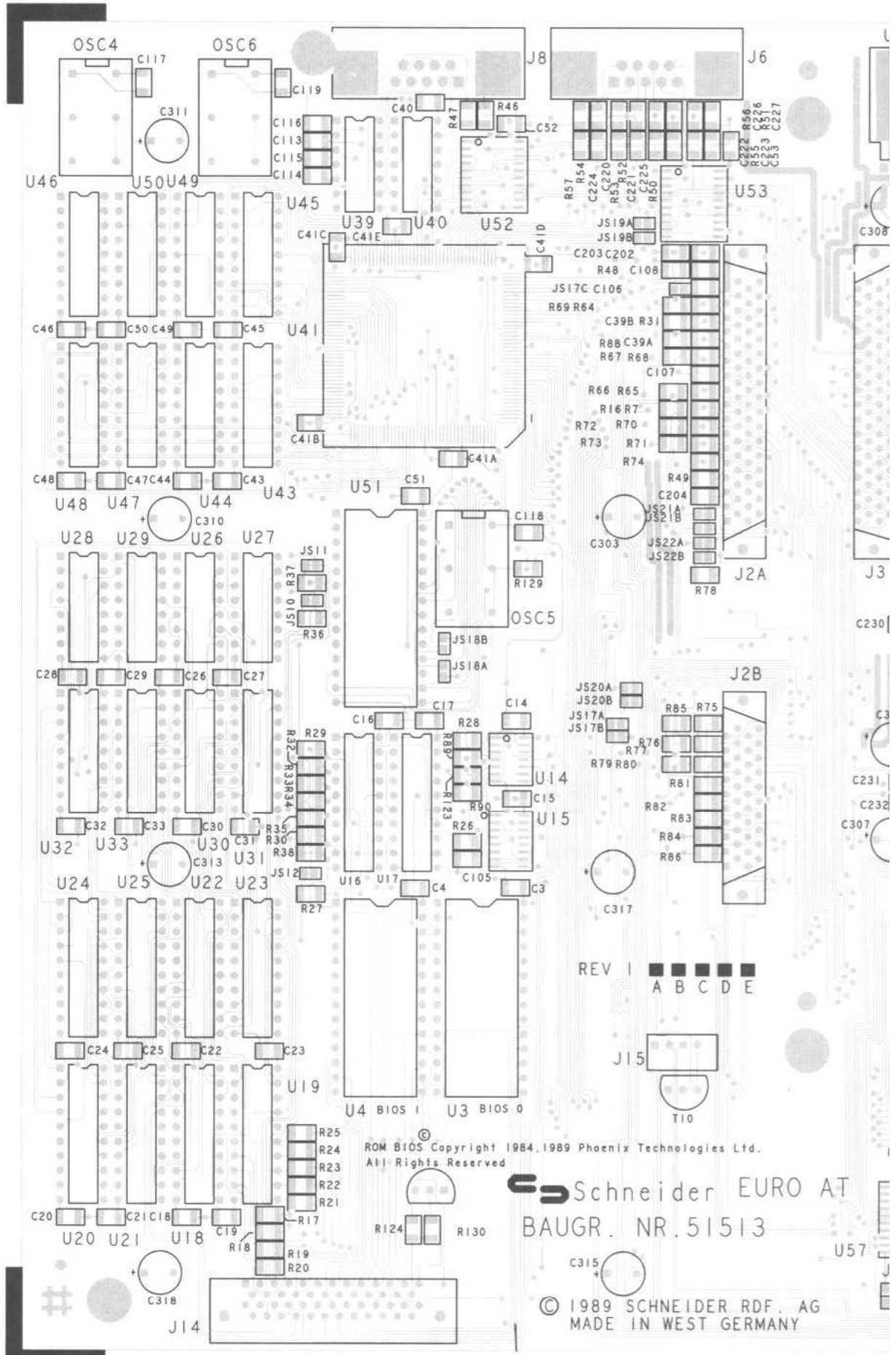
SLOT II

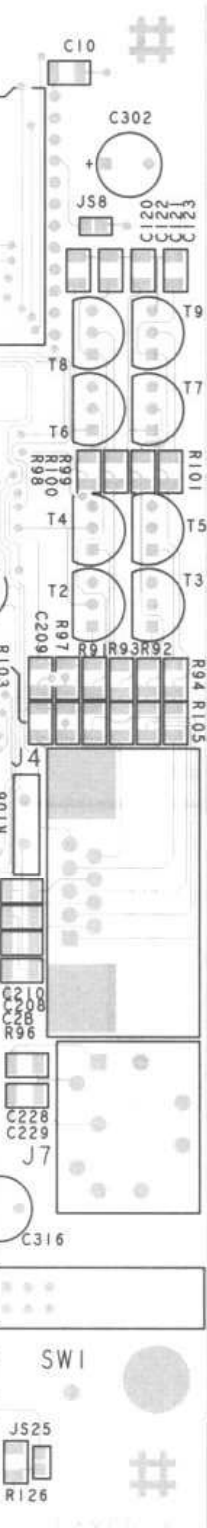
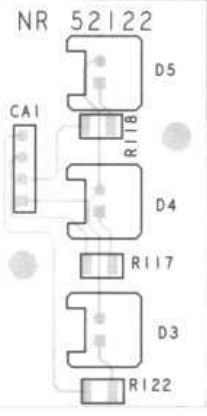
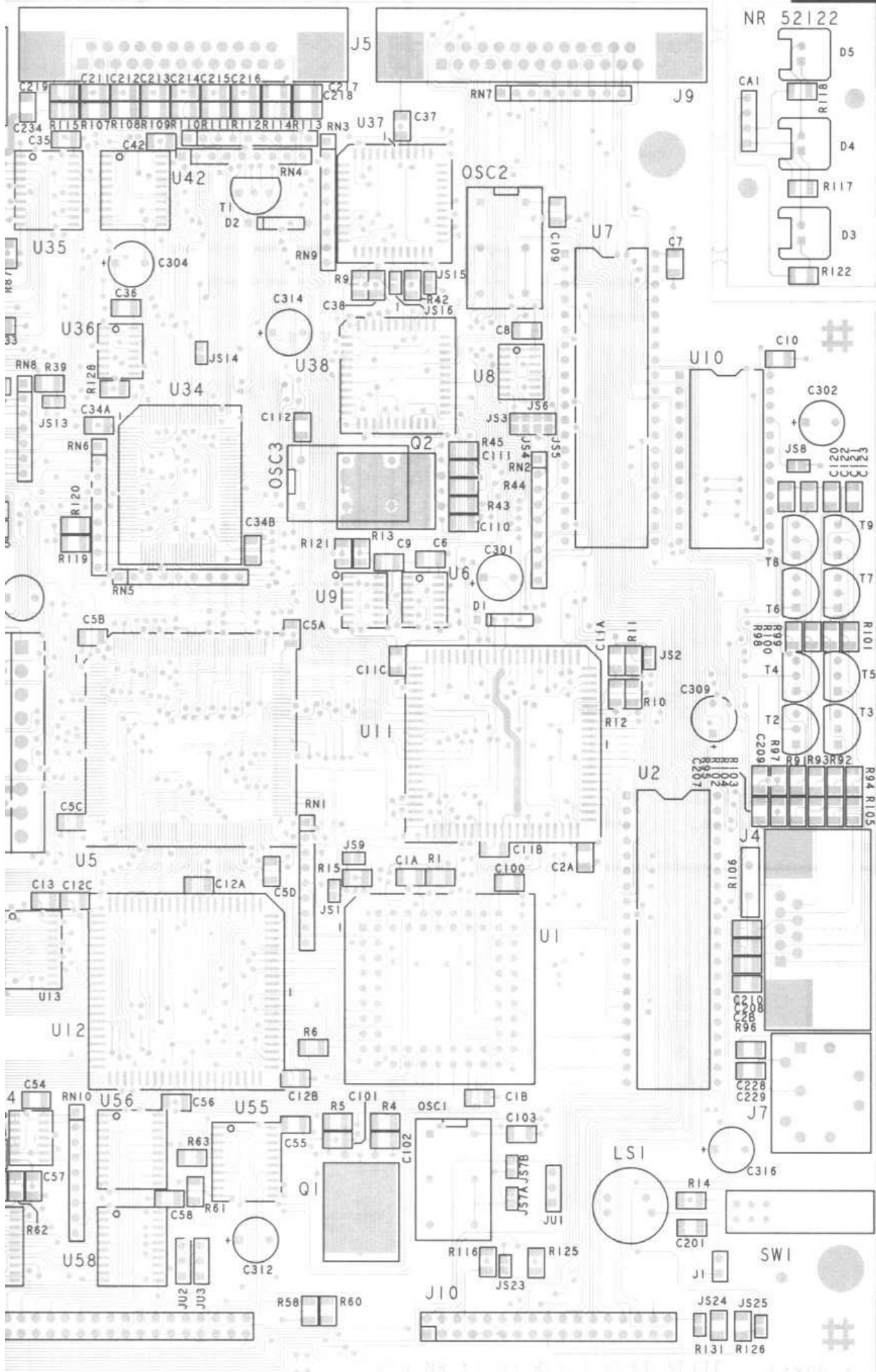
**Schneider** SLT.24

DRWN BY: K. MINZEK  
 APPROVED: [Signature]  
 TITLE: SCHEMATIC  
 SLOTS EURO AT

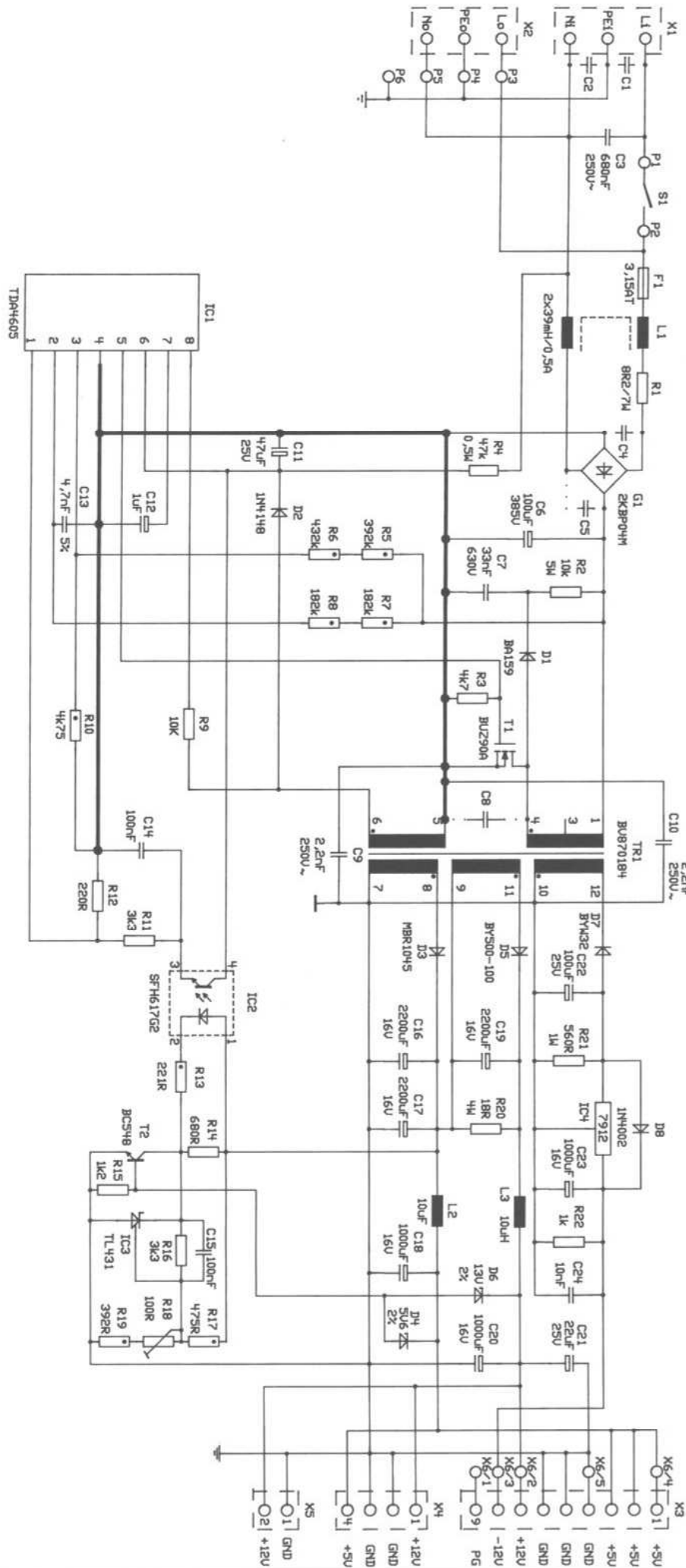
USED ON:	SIZE	REV
EURO AT	A 3	51 620
DATE: 01-29-90	SHEET: 4 of 4	2

Main P.C.B.  
Component side





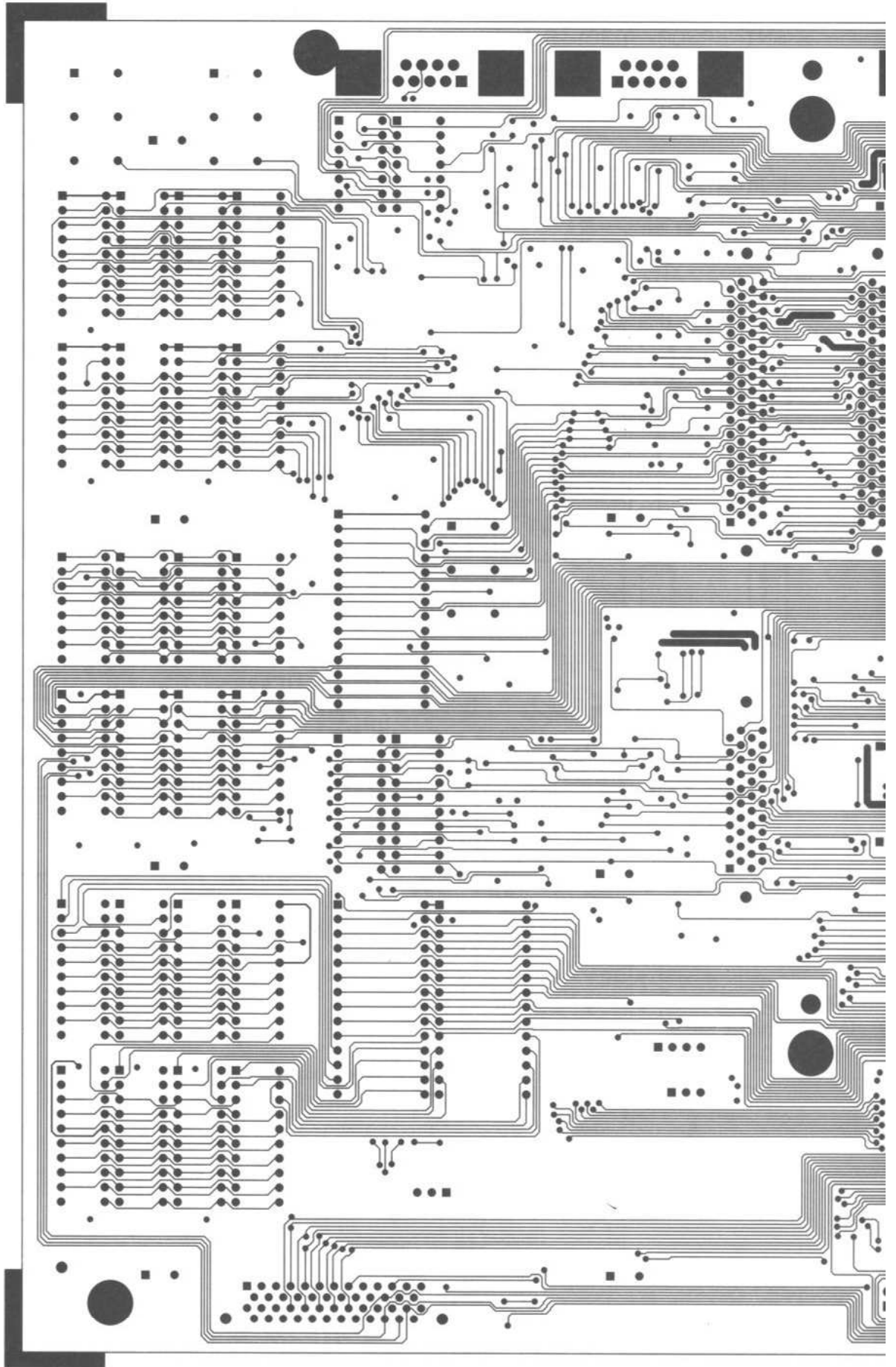
# Schematic diagram Power supply

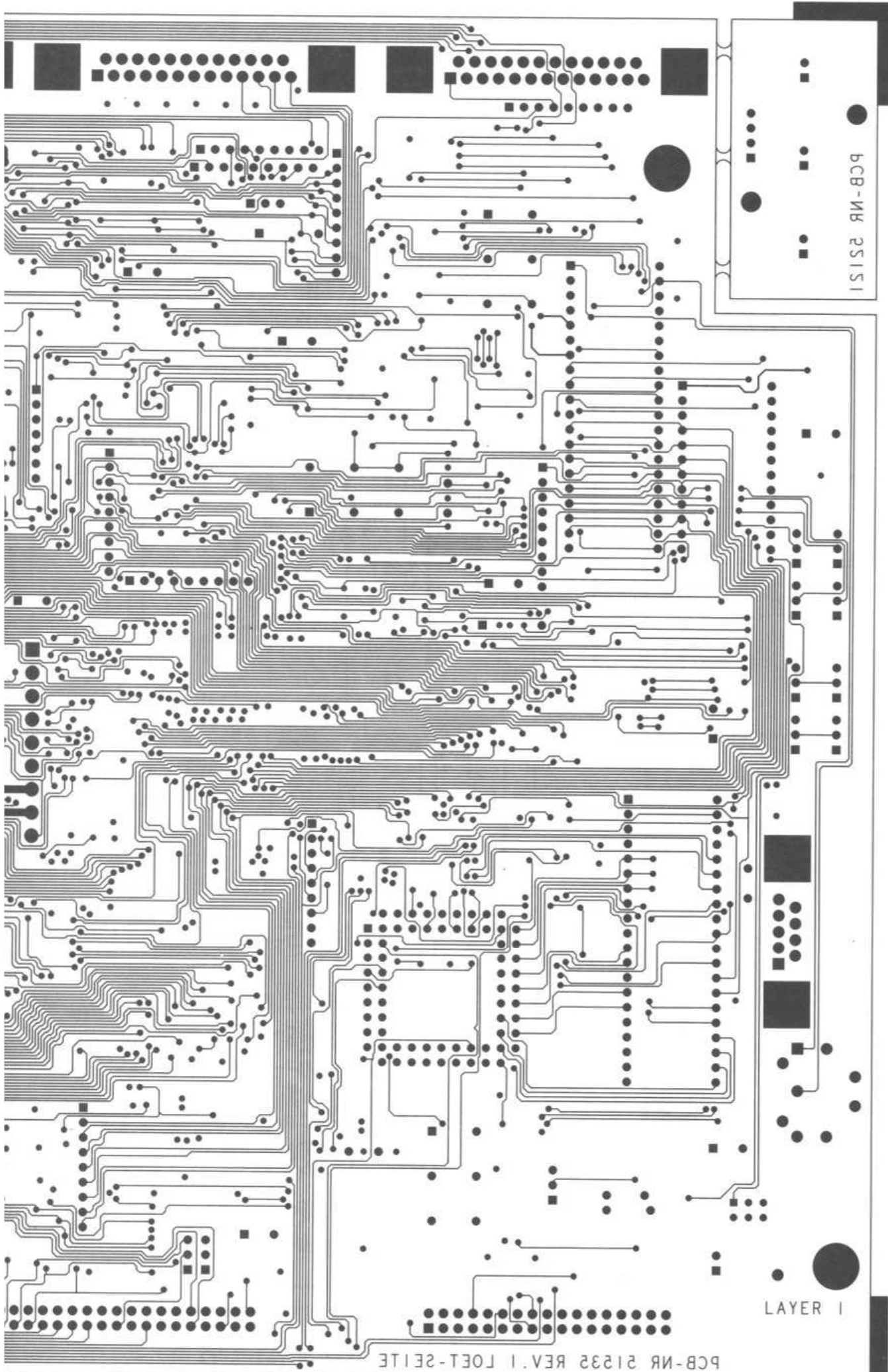


Metallzylinderstand 1x

Platine 81015iv2		Masstab	
		STRONLAUFPLAN	
	Datum	Name	
	28.11.99	Hierse	
	Gepr. 7.2.99		
	Norm		
Zust./Forderung/ Datum Name		Herner Blum - Elektronik	
		D - 8907 Thunhausen	
		5800031	
		SNT60300v2	
		-12V/2A(2,5A)	
		-12V/0,2A	
		BLATT	
		1	

Main P.C.B.  
Soldering side





PCB-NR 21232

LAYER I

PCB-NR 21232 REV. I LOET-SEITE

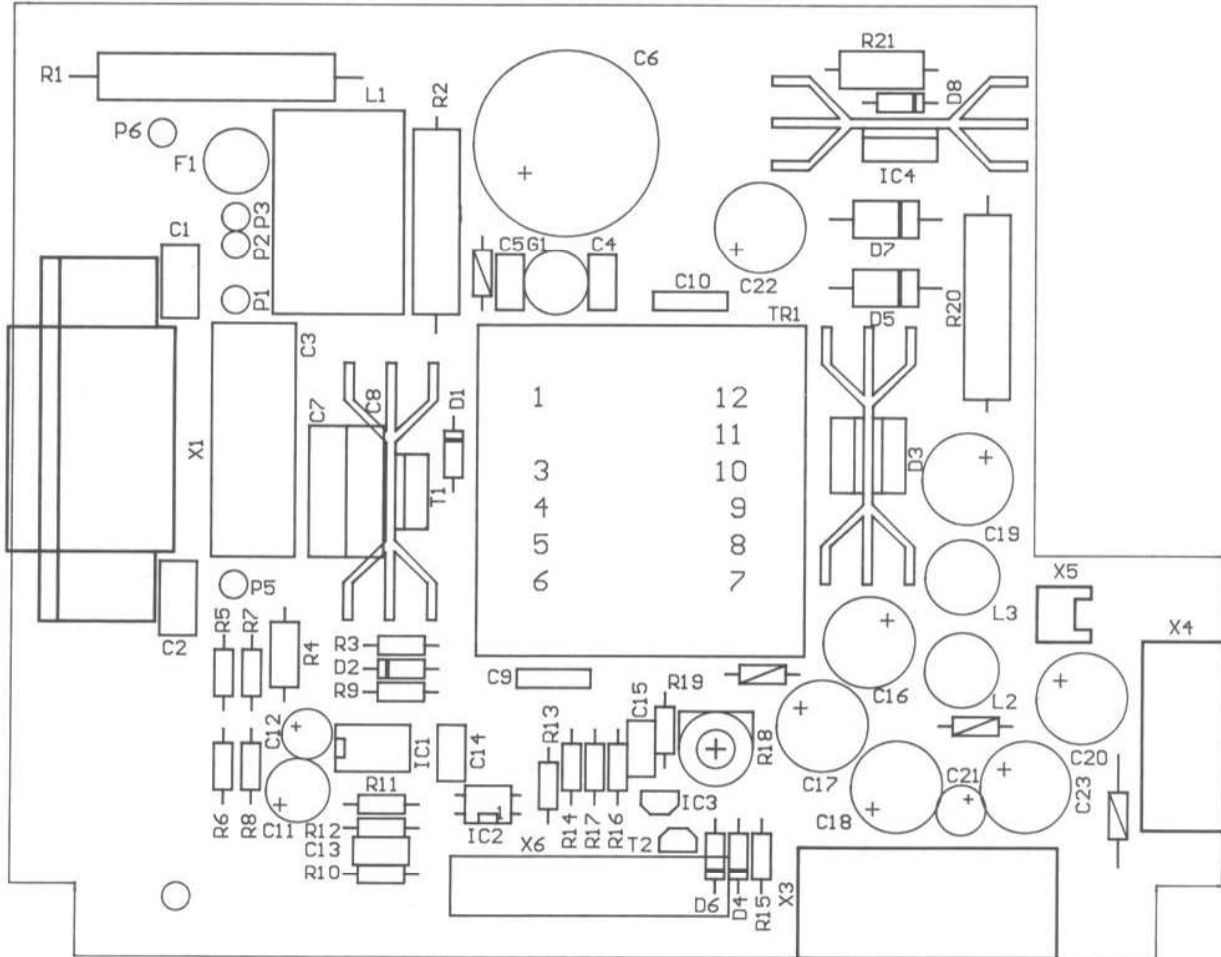


**Power supply**  
**Component side**

SNT 60300/60301 Schneider

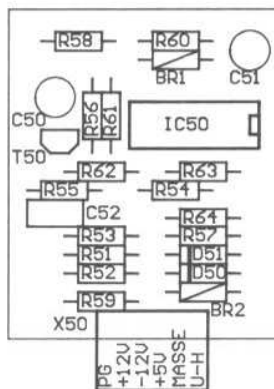
Ident.-Nr.: 810154v2

Groesse: 130\*166 mm



**Power good modul**  
**Component side**

Modul Power-Good fuer SNT 60301/90301  
Ident.-Nr.: 810155  
Groesse: 38\*45 mm

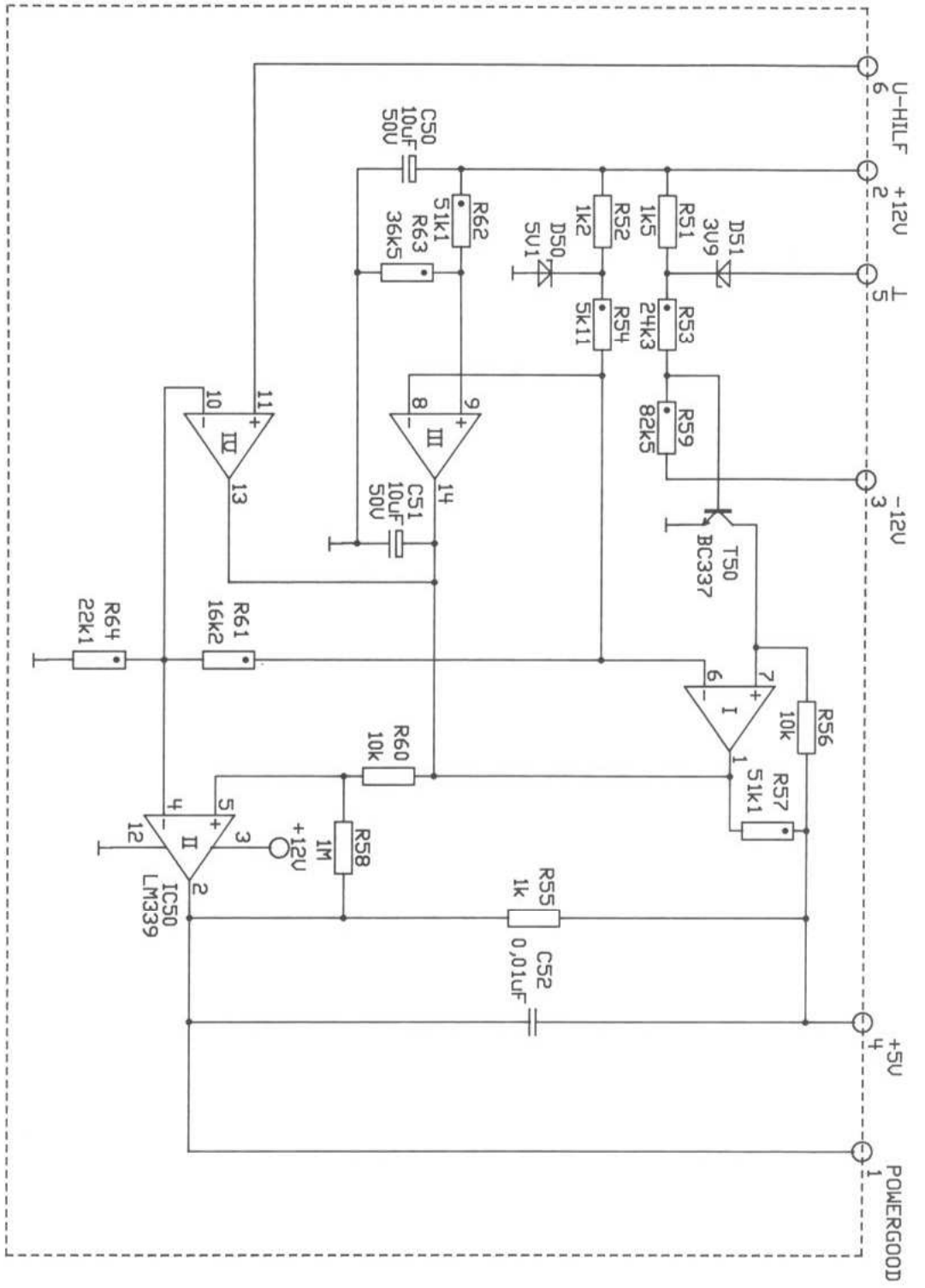


Zust.: Änderung Datum Name

J - 8907 Inannausen

B.

# Schematic diagram Power good modul



Modul 810155		Masstab	
STRIMLAUFLAN			
Datum	Name		
Bearb. 01.12.89	Hafner		
Gepr. 3.2.89	<i>Reiser</i>		
Norm			
Werner Blum - Elektronik			
D - 8907 Thannhausen			
Power-Good-Modul fuer		SNT60301	
		SNT90301	
BLATT			
1			
BL.			

Zust. Aenderung Datum Name

Ersatzteilliste für Platine Best. Euro AT  
Parts List for P.C.B. Euro AT

Best.-Nr. Part.-No.	Bezeichnung	Description	Zeich.-Pos. Ref.-No.	Preisgruppe
50 509 00	IC 80826 CPU 12 MHz	IC 80826 CPU 12 MHz	U 1	F 4
50 444 00	IC EPROM 256K prog.	IC EPROM 256K prog.	U 3	C 9
50 445 00	IC EPROM 256K prog.	IC EPROM 256K prog.	U 4	C 9
50 776 00	IC GC 101A CPU Contr.	IC GC 101A CPU Contr.	U 5	F 5
51 586 00	IC 7406 S014	IC 7406 S014	U 6	A 9
50 243 00	IC 8042 prog.	IC 8042 prog.	U 7	C 3
51 594 00	IC 74 HC 74 S014	IC 74 HC 74 S014	U 8	A 8
51 592 00	IC 74 HC 14 S014	IC 74 HC 14 S014	U 9	A 9
50 177 00	IC DS 1287	IC DS 1287	U 10	D 4
50 046 00	IC GC 102A PLCC	IC GC 102A PLCC	U 11, 12	D 7
51 587 00	IC 74 ALS 245 S020	IC 74 ALS 245 S020	U 13	B 1
51 588 00	IC 74 F 00 S014	IC 74 F 00 S014	U 14	A 7
51 590 00	IC 74 F 175 S016	IC 74 F 175 S016	U 15	A 7
50 334 00	IC HAL 16L8B DIL RAS-PAL	IC HAL 16L8B DIL RAS-PAL	U 16	B 8
50 333 00	IC HAL 16L8B DIL CAS-PAL	IC HAL 16L8B DIL CAS-PAL	U 17	B 8
50 153 00	IC DRAM 256Kx4	IC DRAM 256Kx4	U 18-25	E 9
50 773 00	IC BIG JIM TC110 G008	IC BIG JIM TC110 G008	U 34	D 1
51 319 00	IC 74 LS 245 S020	IC 74 LS 245 S020	U 35	A 7
51 589 00	IC 74 F 126 S014	IC 74 F 126 S014	U 36	A 6
50 105 00	IC WD 37C65B-JM00	IC WD 37C65B-JM00	U 37	D 5
50 827 00	IC 16 C 450 PLCC 44	IC 16 C 450 PLCC 44	U 38	C 6
50 120 00	IC MC 1488	IC MC 1488	U 39	A 5
50 121 00	IC MC 1489	IC MC 1489	U 40	A 5
50 147 00	IC GC 201	IC GC 201	U 41	F 2
51 596 00	IC 74 LS 273 S020	IC 74 LS 273 S020	U 42	A 6
50 110 00	IC DRAM 64Kx4 120nS	IC DRAM 64Kx4 120nS	U 43-50	D 4
50 447 00	IC EPROM 256K prog.	IC EPROM 256K prog.	U 51	C 9
51 591 00	IC 74 F 244 S020	IC 74 F 244 S020	U 52	A 7
51 593 00	IC 74 HC 373 S020	IC 74 HC 373 S020	U 53	A 7
51 595 00	IC 74 LS 10 S014	IC 74 LS 10 S014	U 54	A 5
51 321 00	IC 74 LS 244 S020	IC 74 LS 244 S020	U 55, 58	B 0
51 329 00	IC 74 LS 245 S020	IC 74 LS 245 S020	U 56, 57	A 7
50 238 00	IC 7905 T0220	IC 7905 T0220	U 59	A 6

Ersatzteilliste für Platine Best. Euro AT  
Parts List for P.C.B. Euro AT

Best.-Nr. Part.-No.	Bezeichnung	Description	Zeich.-Pos. Ref.-No.	Preisgruppe
50 358 00	Transistor BC 337 B	Transistor BC 337 B	T 1-5	A 6
51 042 00	Transistor BC 327 B	Transistor BC 327 B	T 6-9	A 2
50 358 00	Transistor BC 337 B	Transistor BC 337 B	T 10, 11	A 6
50 338 00	Diode 1 N 4148	Diode 1 N 4148	D 1, 2	A 2
50 325 00	Quarz 14,31818 MHz HC-18U	Quartz 14.31818 MHz HC-18U	Q 1	B 3
50 312 00	Quarz 1,8432 MHz HC-18U	Quartz 1.8432 MHz HC-18U	Q 2	B 5
50 313 00	Oszillator 16 MHz	Oscillator 16 MHz	OSC 2	C 3
50 337 00	Oszillator 34 MHz	Oscillator 34 MHz	OSC 4	C 0
50 315 00	Oszillator 24 MHz	Oscillator 24 MHz	OSC 5	C 0
50 314 00	Oszillator 19, 20 MHz	Oscillator 19, 20 MHz	OSC 6	C 0
50 316 00	R-Netzwerk 8x10K	R, Network 8x10K	RN 1, 5	A 3
50 319 00	R-Netzwerk 8x4K7	R, Network 8x4K7	RN 2, 4, 9, 10	A 3
50 317 00	R-Netzwerk 8x1K	R, Network 8x1K	RN 3, 7	A 3
51 050 00	R-Netzwerk 8x33K	R, Network 8x33K	RN 6	A 3
50 320 00	R-Netzwerk 6x680K	R, Network 6x680K	RN 8	A 2
50 321 00	PTC PCL 0250,0	PTC PCL 0250.0	R 106	B 0
51 717 00	Resettaste	Pushbutton, reset	SW 1	A 9
50 332 00	Piepser	Buzzer	LS 1	A 8
51 469 00	LED-Kabel 4pol.	Cabel, LED 4-pin	CA 1	A 9
51 520 00	Steckverbindung 40pol.	Socket 40-pin	J 2 B	B 2
51 521 00	Steckverbindung 68pol.	Socket 68-pin	J 2 A, J 3	B 4
51 385 00	Buchse SUB-D 9pol.	Socket SUB-D 9-pin	J 4	A 9
50 133 00	Buchse SUB-D 25pol.	Socket SUB-D 25-pin	J 5	B 2
50 132 00	Buchse SUB-D 9pol.	Socket SUB-D 9-pin	J 6	B 0
51 140 00	Buchse Dioden 5pol.	Socket 5-pin	J 7	A 9
50 131 00	Buchse SUB-D 9pol.	Socket SUB-D 9-pin	J 8	A 9
50 133 00	Buchse SUB-D 25pol.	Socket SUB-D 25-pin	J 9	B 2
51 569 00	Stiftleiste RM 26pol.	Socket RM 26-pin	J 10	A 7
51 431 00	Stiftleiste 9pol.	Socket 9-pin	J 11	A 5
51 085 00	Stiftleiste 40pol.	Socket 40-pin	J 13	A 7
51 616 00	Steckverbindung Leiterplatine	Socket P.C.B.	J 15	A 5

**CAUTION:**

Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the equipment manufacturer. Discard used batteries according to manufacturer's instructions.

Ref.: IEC/74 (Central Office) 104.

## Error Code Table for AT 286

The "DIAGNP OUTPUT" codes are placed at the diagnostic status port to indicate tests in progress and failed tests to someone with hardware which can detect the output. If the FAILBEEP option is selected, the "BEEP CODES" are announced on the speaker if and only if a fatal failure is detected. For instance, "2-1-4" (a burst of two beeps, a single beep, a burst of four beeps) indicates a failure of bit 3 in the first 64K of RAM. Both set of codes are only used prior to screen initialization and screen retrace verification.

Once the screen is believed operable, any diagnostic reporting is via screen messages, except if MANLOOP EQU TRUE and the jumpers say to loop on POST. In this case, it is assumed that no video adapter is attached and some additional errors will be reported via DIAGNP and the speaker.

DIAGNP OUTPUT	BEEP CODES	DESCRIPTION OF TEST OR FAILURE	
01h ;		80286 register test in-progress	
02h ;	1-1-3	CMOS write/read test in-progress or failure	
03h ;	1-1-4	BIOS ROM checksum in-progress or failure	
04h ;	1-2-1	Programmable Interval Timer test in-progress or failure	
05h ;	1-2-2	DMA initialization in-progress or failure	
06h ;	1-2-3	DMA page register write/read test in-progress or fail	
07h ;	1-2-4	JIM port address offset determination in-progress or not found	
08h ;	1-3-1	RAM refresh verification in-progress or failure	
09h ;		1st 64K RAM test in-progress	
0Ah ;	1-3-3	1st 64K RAM chip or data line failure - multi-bit	
0Bh ;	1-3-4	1st 64K RAM odd/even logic failure	
0Ch ;	1-4-1	1st 64K RAM address line failure	
0Dh ;	1-4-2	1st 64K RAM parity test in-progress or failure	
0Eh ;	1-4-3	invalid PBC ID	;V2.00
10h ;	2-1-1	1st 64K RAM chip or data line failure - bit 0	
11h ;	2-1-2	1st 64K RAM chip or data line failure - bit 1	
12h ;	2-1-3	1st 64K RAM chip or data line failure - bit 2	
13h ;	2-1-4	1st 64K RAM chip or data line failure - bit 3	
14h ;	2-2-1	1st 64K RAM chip or data line failure - bit 4	
15h ;	2-2-2	1st 64K RAM chip or data line failure - bit 5	
16h ;	2-2-3	1st 64K RAM chip or data line failure - bit 6	
17h ;	2-2-4	1st 64K RAM chip or data line failure - bit 7	
18h ;	2-3-1	1st 64K RAM chip or data line failure - bit 8	
19h ;	2-3-2	1st 64K RAM chip or data line failure - bit 9	
1Ah ;	2-3-3	1st 64K RAM chip or data line failure - bit A	
1Bh ;	2-3-4	1st 64K RAM chip or data line failure - bit B	
1Ch ;	2-4-1	1st 64K RAM chip or data line failure - bit C	
1Dh ;	2-4-2	1st 64K RAM chip or data line failure - bit D	
1Eh ;	2-4-3	1st 64K RAM chip or data line failure - bit E	
1Fh ;	2-4-4	1st 64K RAM chip or data line failure - bit F	
20h ;	3-1-1	slave DMA register test in-progress or failure	
21h ;	3-1-2	master DMA register test in-progress or failure	
22h ;	3-1-3	master interrupt mask register test in-progress or fail	
23h ;	3-1-4	slave interrupt mask register test in-progress or fail	
25h ;		interrupt vector loading in-progress	
27h ;	3-2-4	keyboard controller test in-progress or failure	
28h ;		CMOS power-fail and checksum checks in-progress	
29h ;		CMOS config info validation in-progress	
2Ah ;	3-3-3	monitor type determination in-progress or failure	
2Bh ;	3-3-4	screen memory test in-progress or failure	
2Ch ;	3-4-1	screen initialization in-progress or failure	
2Dh ;	3-4-2	screen retrace tests in-progress or failure	
2Eh ;		search for video ROM in-progress	
30h ;		screen believed operable:	

---

30h ; screen believed running w/ video ROM  
31h ; monochromatic screen believed operable  
32h ; 40-column color screen believed operable  
33h ; 80-column color screen believed operable

The following codes are reported via DIAGNP and the speaker only if  
MANLOOP EQU TRUE and the "manufacturing jumper" indicates loop on POST.  
Otherwise, these errors are reported via the screen and POST continues.  
Use of the manufacturing jumper requires working correctly configured CMOS.

34h ; 4-2-1 timer tick interrupt test in\_progress or failure  
35h ; 4-2-2 shutdown test in\_progress or failure  
36h ; 4-2-3 gate A20 failure  
37h ; 4-2-4 unexpected interrupt in protected mode  
38h ; 4-3-1 RAM test in\_progress or failure above address 0FFFFh  
3Ah ; 4-3-3 Interval timer channel 2 test in\_progress or failure  
3Bh ; 4-3-4 Time-Of-Day clock test in\_progress or failure  
3Ch ; 4-4-1 Serial port test test in\_progress or failure  
3Dh ; 4-4-2 Parallel port test test in\_progress or failure  
3Eh ; 4-4-3 Math Coprocessor test in\_progress or failure

\*\*\*\*\* **ACHTUNG!** \*\*\*\*\*

Für Daten aller Art, die sich bei Einsendung an die Firma Schneider im Servicefall oder in jedem anderen Fall noch auf einem Datenträger befinden, wird bei Verlust oder Beschädigung keinerlei Haftung übernommen!

Bitte, sichern Sie sich Ihre Daten vorher von Ihrem Datenträger. Wie man eine Sicherungskopie erstellt, können Sie in Ihrer MS-DOS-Betriebsanleitung nachlesen.

\*\*\*\*\* **IMPORTANT!** \*\*\*\*\*

We accept no responsibility for the loss of or damage to data of any kind which is still contained on a data carrier which is sent to Schneider for servicing or for any other reason!

Please copy your data from your data carrier beforehand. You can read how to produce a backup copy in your MS-DOS operating instructions.

---

Bitte bei Ersatzteilbestellung die genaue Bezeichnung und Ident-Nr. (siehe Typenschild) des Gerätes sowie Bestell-Nummer und Positions-Nummer des Ersatzteils angeben.

For ordering of spare parts please state exact description and ident no. of unit (see silver rating label on the backside of unit) as well as part no. and position no. of required spare parts.

Benutzen Sie:

Telex: 531 516

oder



\* 317298 #

oder

Telefax: 08245/5 1326